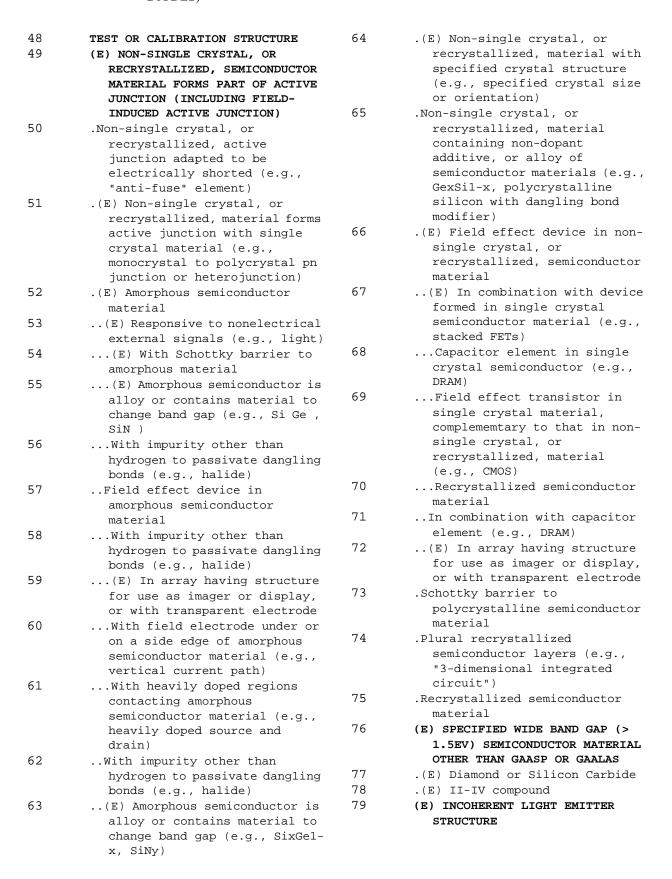
| | | 19 | (E) Si Ge |
|------------------------------------------|-------------------------------------|-----|----------------------------------|
| When nla | cing a mandatory classification in | 20 | (E) Field effect device |
| a USPC subclass having a title beginning | | 21 | (E) Light responsive |
| | E), " a cross-reference classifica- | 21 | structure |
| tion is | normally made in at least one of | 22 | (E) With specified |
| the appe | ended E-subclasses. | | semiconductor materials |
| | | 23 | (E) Current flow across well |
| | | 24 | (E) Field effect device |
| | | 25 | (E) Employing resonant |
| 1 | (D) DIE E DEDECE DELL'ED | 23 | tunneling |
| 1 | (E) BULK EFFECT DEVICE | 26 | |
| 2 | .(E) Bulk effect switching in | | (E) Ballistic transport device |
| | amorphous material | 27 | (E) Field effect transistor |
| 3 | (E) With means to localize | 28 | .Non-heterojunction superlattice |
| | region of conduction (e.g., | | (e.g., doping superlattice or |
| | "pore" structure) | | alternating metal and |
| 4 | (E) With specified electrode | | insulator layers) |
| | composition or configuration | 29 | .Ballistic transport device |
| 5 | (E) In array | | (e.g., hot electron |
| 6 | .(E) Intervalley transfer (e.g., | | transistor) |
| O | Gunn effect) | 30 | .Tunneling through region of |
| 7 | • | 30 | reduced conductivity |
| 7 | (E) In monolithic integrated | 31 | Josephson |
| | circuit | | - |
| 8 | (E) Three or more terminal | 32 | Particular electrode material |
| | device | 33 | High temperature (i.e., >30 |
| 9 | (E) THIN ACTIVE PHYSICAL LAYER | | Kelvin) |
| | WHICH IS (1) AN ACTIVE | 34 | Weak link (e.g., narrowed |
| | POTENTIAL WELL LAYER THIN | | portion of superconductive |
| | ENOUGH TO ESTABLISH DISCRETE | | line) |
| | QUANTUM ENERGY LEVELS OR (2) | 35 | Particular barrier material |
| | AN ACTIVE BARRIER LAYER THIN | 36 | With additional electrode to |
| | ENOUGH TO PERMIT QUANTUM | | control conductive state of |
| | MECHANICAL TUNNELING OR (3) AN | | Josephson junction |
| | ACTIVE LAYER THIN ENOUGH TO | 37 | At least one electrode layer of |
| | PERMIT CARRIER TRANSMISSION | 57 | semiconductor material |
| | WITH SUBSTANTIALLY NO | 20 | |
| | SCATTERING (E.G., | 38 | Three or more electrode device |
| | SUPERLATTICE, QUANTUM WELL, OR | 39 | Three or more electrode device |
| | BALLISTIC TRANSPORT DEVICE) | 40 | (E) ORGANIC SEMICONDUCTOR |
| 1.0 | .Low workfunction layer for | | MATERIAL |
| 10 | - | 41 | (E) POINT CONTACT DEVICE |
| | electron emission, e.g., | 42 | (E) SEMICONDUCTOR IS SELENIUM OR |
| | photocathode electron emissive | | TELLURIUM IN ELEMENTAL FORM |
| | layer | 43 | (E) SEMICONDUCTOR IS AN OXIDE OF |
| 11 | Combined with a heterojunction | | A METAL (E.G., CUO, ZNO) OR |
| | involving a III-V compound | | COPPER SULFIDE |
| 12 | .(E) Heterojunction | 44 | WITH METAL CONTACT ALLOYED TO |
| 13 | (E) Incoherent light emitter | 11 | ELEMENTAL SEMICONDUCTOR TYPE |
| 14 | (E) Quantum well | | PN JUNCTION IN NONREGENERATIVE |
| 15 | (E) Superlattice | | |
| 16 | (E) Of amorphous | 4 - | STRUCTURE |
| ± 0 | semiconductor material | 45 | .Elongated alloyed region (e.g., |
| 17 | | | thermal gradient zone melting, |
| 17 | (E) With particular barrier | | TGZM) |
| | dimension | 46 | .In pn junction tunnel diode |
| 18 | (E) Strained layer | | (Esaki diode) |
| | superlattice | 47 | .In bipolar transistor structure |
| | | | |

257 - 2 CLASS 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE DIODES)



| 80 | .In combination with or also constituting light responsive | 103 | .(E) With particular semiconductor material |
|----------|--------------------------------------------------------------------------|---------|-----------------------------------------------------------------|
| 81 | device | 104 | (E) TUNNELING PN JUNCTION (E.G., |
| OΤ | With specific housing or contact structure | 105 | ESAKI DIODE) DEVICE .In three or more terminal device |
| 82 | Discrete light emitting and | 105 | .(E) Reverse bias tunneling |
| 02 | light responsive devices | 100 | structure (e.g., "backward" |
| 83 | Light coupled transistor | | diode, true Zener diode) |
| | structure | 107 | (E) REGENERATIVE TYPE SWITCHING |
| 84 | Combined in integrated structure | | DEVICE (E.G., SCR, COMFET, THYRISTOR) |
| 85 | With heterojunction | 108 | .Controlled by nonelectrical, |
| 86 | .Active layer of indirect band | | nonoptical external signal |
| | gap semiconductor | | (e.g., magnetic field, |
| 87 | With means to facilitate | | pressure, thermal) |
| | electron-hole recombination | 109 | .Having only two terminals and no |
| | (e.g., isoelectronic traps | | control electrode (gate) |
| | such as nitrogen in GaP) | | (e.g., Shockley diode) |
| 88 | .(E) Plural light emitting | 110 | More than four semiconductor |
| | devices (e.g., matrix, 7- | | layers of alternating |
| 0.0 | segment array) | | conductivity types (e.g., |
| 89 | (E) Multi-color emission | | <pre>pnpnpn structure, 5 layer bidirectional diacs, etc.)</pre> |
| 90 91 | (E) With heterojunction | 111 | Triggered by VBO overvoltage |
| 91 | <pre>(E) With shaped contacts or opaque masking</pre> | | means |
| 92 | (E) Alphanumeric segmented | 112 | (E) With highly-doped breakdown |
| | array | | diode trigger |
| 93 | (E) With electrical isolation | 113 | .With light activation |
| | means in integrated circuit | 114 | (\mathtt{E}) With separate light dector |
| | structure | | integrated on chip with |
| 94 | .With heterojunction | | regenerative switching device |
| 95 | With contoured external surface | 115 | (E) With electrical trigger |
| | (e.g., dome shape to | | signal amplification means |
| | facilitate light emission) | | <pre>(e.g., amplified gate, "pilot thyristor", etc.)</pre> |
| 96 | (E) Plural heterojunctions in | 116 | (E) With light conductor means |
| 0.17 | same device | 110 | (e.g., light fiber or light |
| 97 | (E) More than two | | pipe) integral with device or |
| 0.0 | heterojunctions in same device | | device enclosure or package |
| 98 | <pre>.(E) With reflector, opaque mask, or optical element (e.g.,</pre> | 117 | (E) In groove or with thinned |
| | lens, optical fiber, index of | | semiconductor portion |
| | refraction matching layer, | 118 | (E) With groove or thinned |
| | luminescent material layer, | | light sensitive portion |
| | filter) integral with device | 119 | .(E) Bidirectional rectifier with |
| | or device enclosure or package | | control electrode (gate) |
| 99 | .(E) With housing or contact | | (e.g., Triac) |
| | structure | 120 | Six or more semiconductor |
| 100 | .(E) Encapsulated | | layers of alternating |
| 101 | .(E) With particular dopant | | conductivity types (e.g., |
| | concentration or concentration | 121 | <pre>npnpnpn structure)(E) With diode or transistor in</pre> |
| | profile (e.g., graded | 1 | reverse path |
| 100 | junction) | 122 | (E) Lateral |
| 102 | <pre>.(E) With particular dopant material (e.g., Zinc as dopant</pre> | | (1) 1400141 |
| | in GaAs) | | |
| | | | |

257 - 4 CLASS 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE DIODES)

| 123 | <pre>(E) With trigger signal amplification (e.g., amplified</pre> | 147 | .(E) With extended latchup current level (e.g., gate turn |
|-----|-------------------------------------------------------------------|-----|-----------------------------------------------------------|
| 124 | <pre>gate)(E) Combined with field effect</pre> | 148 | off "GTO" device)Having impurity doping for gain |
| | transistor structure | | reduction |
| 125 | \dots (E) Controllable emitter | 149 | Having anode shunt means |
| | shunting | 150 | With specified housing or |
| 126 | With means to separate a device | | external terminal |
| | <pre>into sections having different conductive polarity</pre> | 151 | External gate terminal structure or composition |
| 127 | Guard ring or groove | 152 | Cathode emitter or cathode |
| 128 | Having overlapping sections of | | electrode feature |
| | different conductive polarity | 153 | Gate region or electrode |
| 129 | With means to increase reverse | | feature |
| | breakdown voltage | 154 | .(E) With resistive region |
| 130 | Switching speed enhancement | | connecting separate sections |
| | means | | of device |
| 131 | Recombination centers or deep | 155 | .With switching speed enhancement |
| | level dopants | | means (e.g., Schottky contact) |
| 132 | Five or more layer | 156 | Having deep level dopants or |
| | unidirectional structure | | recombination centers |
| 133 | .(E) Combined with field effect | 157 | .(E) With integrated trigger |
| 133 | transistor | 13, | signal amplification means |
| 134 | (E) J-FET (junction field | | (e.g., amplified gate, "pilot |
| 131 | effect transistor) | | thyristor", etc.) |
| 135 | (E) Vertical (i.e., where the | 158 | (E) Three or more amplification |
| 133 | source is located above the | 130 | stages |
| | drain or vice versa) | 159 | (E) Transistor as amplifier |
| 136 | (E) Enhancement mode (e.g., | 160 | (E) With distributed amplified |
| 130 | so-called SITs) | 100 | current |
| 137 | (E) Having controllable emitter | 161 | (E) With a turn-off diode |
| 137 | shunt | 162 | (E) with a turn-off drode .Lateral structure |
| 138 | | 163 | .Emitter region feature |
| 130 | (E) Having gate turn off (GTO) feature | 164 | |
| 139 | (E) With extended latchup | 104 | Multi-emitter region (e.g., |
| 139 | current level (e.g., COMFET | | <pre>emitter geometry or emitter ballast resistor)</pre> |
| | device) | 165 | Laterally symmetric regions |
| 140 | (E) Combined with other solid | 166 | Radially symmetric regions |
| | state active device in | 167 | .Having at least four external |
| | integrated structure | | electrodes |
| 141 | (E) Lateral structure, i.e., | 168 | .With means to increase breakdown |
| | current flow parallel to main | | voltage |
| | device surface | 169 | High resistivity base layer |
| 142 | \dots (E) Having impurity doping for | 170 | Surface feature (e.g., guard |
| | gain reduction | | ring, groove, mesa) |
| 143 | \dots (E) Having anode shunt means | 171 | Edge feature (e.g., beveled |
| 144 | \dots (E) Cathode emitter or cathode | | edge) |
| | electrode feature | 172 | .With means to lower "ON" voltage |
| 145 | (E) Low impedance channel | | drop |
| | contact extends below surface | 173 | .(E) Device protection (e.g., |
| 146 | .(E) Combined with other solid | | from overvoltage) |
| | state active device in | 174 | Rate of rise of current (e.g., |
| | integrated structure | | dI/dt) |
| | | | |

| 175 | .With means to control triggering (e.g., gate electrode configuration, zener diode firing, dV/dt control, transient control by ferrite bead, etc.) | 197 198 199 | .(E) Bipolar transistor.Wide band gap emitter.(E) Avalanche diode (e.g., socalled "Zener" diode having breakdown voltage greater than 6 volts, including |
|-------|----------------------------------------------------------------------------------------------------------------------------------------------------|-------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 176 | <pre>Located in an emitter-gate region</pre> | | heterojunction IMPATT type microwave diodes) |
| 177 | .With housing or external electrode | 200 | .(E) Heterojunction formed between semiconductor |
| 178 | With means to avoid stress between electrode and active device (e.g., thermal expansion matching of electrode to semiconductor) | | materials which differ in that they belong to different periodic table groups (e.g., Ge (group IV) - GaAs (group III-V) or InP (group III-V) - |
| 179 | With malleable electrode (e.g., silver electrode layer) | 201 | CdTe (group II-VI)) .(E) Between different group IV- |
| 180 | Stud mount | | VI or II-VI or III-V compounds other than GaAs/GaAlAs |
| 181 | With large area flexible | 202 | (E) GATE ARRAYS |
| | electrodes in press contact with opposite sides of active | 202 | .(E) With particular chip input/ |
| | semiconductor chip and | 203 | output means |
| | surrounded by an insulating | 204 | .(E) Having specific type of |
| | element (e.g., ring) | 201 | active device (e.g., CMOS) |
| 182 | With lead feedthrough means on | 205 | (E) With bipolar transistors or |
| | side of housing | 203 | with FETs of only one channel |
| 183 | (E) HETEROJUNCTION DEVICE | | conductivity type (e.g., |
| 183.1 | .Charge transfer device | | enhancement-depletion FETs) |
| 184 | .(E) Light responsive structure | 206 | (E) Particular layout of |
| 185 | (E) Staircase (including graded | | complementary FETs with regard |
| | composition) device | | to each other |
| 186 | (E) Avalanche photodetection | 207 | .(E) With particular power supply |
| | structure | | distribution means |
| 187 | (E) Having transistor structure | 208 | .(E) With particular signal path |
| 188 | (E) Having narrow energy band | | connections |
| | <pre>gap (<<1eV) layer (e.g.,</pre> | 209 | <pre>(E) Programmable signal paths (e.g., with fuse elements,</pre> |
| 189 | PbSnTe, HgCdTe, etc.)(E) Layer is a group III-V | | laser programmable, etc.) |
| 109 | semiconductor compound | 210 | (E) With wiring channel area |
| 190 | .With lattice constant mismatch | 211 | (E) Multi-level metallization |
| 100 | (e.g., with buffer layer to | 212 | (E) CONDUCTIVITY MODULATION |
| | accomodate mismatch) | | DEVICE (E.G., UNIJUNCTION |
| 191 | .Having graded composition | | TRANSISTOR, DOUBLE-BASE DIODE, |
| 192 | .(E) Field effect transistor | | CONDUCTIVITY-MODULATED |
| 194 | (E) Doping on side of | | TRANSISTOR) |
| 174 | heterojunction with lower | 213 | (E) FIELD EFFECT DEVICE |
| | carrier affinity (e.g., high | 214 | .(E) Charge injection device |
| | electron mobility transistor | 215 | .(E) Charge transfer device |
| | (HEMT) | 216 | (E) Majority signal carrier |
| 195 | (E) Combined with diverse type device | | <pre>(e.g., buried or bulk channel, or peristaltic)</pre> |
| 196 | .Both semiconductors of the | 217 | (E) Having a conductive means |
| 190 | heterojunction are the same conductivity type (i.e., either N or P) | 217 | in direct contact with channel (e.g., non-insulated gate) |

257 - 6 CLASS 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE DIODES)

| 218 | (E) High resistivity channel (e.g., accumulation mode) or | 236 | Signal applied to field effect electrode |
|-------------|-----------------------------------------------------------|-------|------------------------------------------|
| | surface channel (e.g., | 237 | Charge-presetting/linear |
| | transfer of signal charge | | input type (e.g., fill and |
| | occurs at the surface of the | | spill) |
| | semi-conductor) or minority | 238 | Input signal responsive to |
| | carriers at input (e.g., | 250 | signal charge in charge |
| | surface channel input) | | transfer device (e.g., |
| 219 | Impurity concentration | | regeneration or feedback) |
| | variation | 239 | |
| 220 | Vertically within channel | 239 | Signal charge detection type |
| 220 | (e.g., profiled) | | (e.g., floating diffusion or |
| 221 | Along the length of the | | floating gate non-destructive |
| 221 | | 240 | output) |
| | channel (e.g., doping | 240 | Changing width or direction of |
| | variations for transfer | | channel (e.g., meandering |
| 222 | directionality) | 0.41 | channel) |
| 222 | (E) Responsive to non- | 241 | Multiple channels (e.g., |
| | electrical external signal | | converging or diverging or |
| | (e.g., imager) | | parallel channels) |
| 223 | \dots (E) Having structure to | 242 | Vertical charge transfer |
| | improve output signal (e.g., | 243 | Channel confinement |
| | antiblooming drain) | 244 | Comprising a groove |
| 224 | Channel confinement | 245 | Structure for applying electric |
| 225 | (E) Non-electrical input | | field into device (e.g., |
| | responsive (e.g., light | | resistive electrode, acoustic |
| | responsive imager, input | | traveling wave in channel) |
| | programmed by size of storage | 246 | Phase structure (e.g., doping |
| | sites for use as a read-only | | variations to provide |
| | memory, etc.) | | asymmetry for 2-phase |
| 226 | \dots (E) Sensor element and charge | | operation; more than four |
| | transfer device are of | | phases or "electrode per bit") |
| | different materials or on | 247 | Uniphase or virtual phase |
| | different substrates (e.g., | | structure |
| | "hybrid") | 248 | (E) 2-phase |
| 227 | (E) With specified dopant | 249 | Electrode structures or |
| | (e.g., photoionizable, | | materials |
| | "extrinsic" detectors for | 250 | Plural gate levels |
| | infrared) | 251 | (E) Substantially incomplete |
| 228 | (E) Light responsive, back | 231 | signal charge transfer (e.g., |
| | illuminated | | bucket brigade) |
| 229 | (E) Having structure to | 252 | Responsive to non-optical, non- |
| | improve output signal (e.g., | 232 | electrical signal |
| | exposure control structure) | 252 | _ |
| 230 | (E) With blooming suppression | 253 | Chemical (e.g., ISFET, CHEMFET) |
| | structure | 254 | Physical deformation (e.g., |
| 231 | (E) 2-dimensional area | | strain sensor, acoustic wave |
| | architecture | 0.5.5 | detector) |
| 232 | (E) Having alternating strips | 255 | .With current flow along |
| 232 | of sensor structures and | | specified crystal axis (e.g., |
| | register structures (e.g., | | axis of maximum carrier |
| | interline imager) | | mobility) |
| 233 | (E) Sensors not overlaid by | 256 | .(E) Junction field effect |
| 200 | electrode (e.g., photodiodes) | | transistor (unipolar |
| 234 | | | transistor) |
| 43 4 | (E) Single strip sensors | | |
| 235 | <pre>(e.g., linear imager)Electrical input</pre> | | |
| 433 | mrectricar input | | |

| 257 | (E) Light responsive or | 277 | (E) With capacitive or |
|-----|---------------------------------------------------------|------|---------------------------------------------------|
| | combined with light responsive | 0.00 | inductive elements |
| 050 | device | 278 | (E) With devices vertically |
| 258 | (E) In imaging array | | spaced in different layers of |
| 259 | Elongated active region acts as | | semiconductor material (e.g., |
| | transmission line or | | "3-dimensional" integrated |
| | distributed active element | 270 | circuit) |
| | (e.g., "transmission line" | 279 | Pn junction gate in compound |
| 260 | field effect transistor)Same channel controlled by both | | semiconductor material (e.g., |
| 200 | junction and insulated gate | 280 | GaAs) |
| | electrodes, or by both | 281 | With Schottky gate |
| | Schottky barrier and pn | 201 | Schottky gate to silicon semiconductor |
| | junction gates (e.g., "taper | 282 | |
| | isolated memory cell) | 282 | Gate closely aligned to source |
| 261 | Junction gate region free of | 283 | region |
| 201 | direct electrical connection | 283 | With groove or overhang for |
| | (e.g., floating junction gate | 284 | alignment |
| | memory cell structure) | _ | Schottky gate in groove |
| 262 | (E) Combined with insulated | 285 | With profiled channel dopant |
| | gate field effect transistor | | concentration or profiled gate |
| | (IGFET) | | region dopant concentration (e.g., maximum dopant |
| 263 | Vertical controlled current | | concentration below surface) |
| | path | 286 | With non-uniform channel |
| 264 | Enhancement mode or with high | 200 | thickness or width |
| | resistivity channel (e.g., | 287 | With multiple channels or |
| | doping of 1015cm-3 or less) | 207 | chanel segments connected in |
| 265 | (E) In integrated circuit | | parallel, or with channel much |
| 266 | (E) With multiple parallel | | wider than length between |
| | current paths (e.g., grid | | source and drain (e.g., power |
| | gate) | | JFET) |
| 267 | (E) With Schottky barrier | 288 | .(E) Having insulated electrode |
| | gate | | (e.g., MOSFET, MOS diode) |
| 268 | Enhancement mode | 289 | (E) Significant semiconductor |
| 269 | With means to adjust barrier | | chemical compound in bulk |
| | height (e.g., doping profile) | | crystal (e.g., GaAs) |
| 270 | Plural, separately connected, | 290 | (E) Light responsive or |
| | gates control same channel | | combined with light responsive |
| | region | | device |
| 271 | (E) Load element or constant | 291 | (E) Imaging array |
| | current source (e.g., with | 292 | \dots (E) Photodiodes accessed by |
| | source to gate connection) | | FETs |
| 272 | (E) Junction field effect | 293 | (E) Photoresistors accessed |
| | transistor in integrated | | by FETs, or photodetectors |
| | circuit | | separate from FET chip |
| 273 | \dots (E) With bipolar device | 294 | \dots (E) With shield, filter, or |
| 274 | \ldots (E) Complementary junction | | lens |
| | field effect transistors | 295 | (\mathtt{E}) With ferroelectric material |
| 275 | (E) Microwave integrated | | layer |
| | circuit (e.g., microstrip | 296 | (\mathtt{E}) Insulated gate capacitor or |
| | type) | | insulated gate transistor |
| 276 | \dots (E) With contact or heat sink | | combined with capacitor (e.g., |
| | extending through hole in | | dynamic memory cell) |
| | semiconductor substrate, or | | |
| | with electrode suspended over | | |
| | <pre>substrate (e.g., air bridge)</pre> | | |

257 - 8 CLASS 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE DIODES)

| 297 | With means for preventing charge leakage due to minority carrier generation (e.g., alpha generated soft error | 317 | <pre>With irregularities on electrode to facilitate charging or discharging of floating electrode</pre> |
|-----------------|---------------------------------------------------------------------------------------------------------------|-----|---------------------------------------------------------------------------------------------------------------|
| | protection or "dark current" leakage protection) | 318 | Additional control electrode is doped region in |
| 298 | (E) Capacitor for signal | | semiconductor substrate |
| | storage in combination with | 319 | Plural additional contacted |
| | non-volatile storage means | 31) | control electrodes |
| 299 | (E) Structure configured for | 320 | |
| 299 | | 320 | Separate control electrodes |
| | voltage converter (e.g., | | for charging and for |
| | charge pump, substrate bias | | discharging floating electrode |
| | generator) | 321 | With thin insulator region |
| 300 | Capacitor coupled to, or forms | | for charging or discharging |
| | gate of, insulated gate field | | floating electrode by quantum |
| | effect transistor (e.g., | | mechanical tunneling |
| | nondestructive readout dynamic | 322 | With charging or discharging |
| | memory cell structure) | | by control voltage applied to |
| 301 | Capacitor in trench | | source or drain region (e.g., |
| 302 | Vertical transistor | | by avalanche breakdown of |
| 303 | Stacked capacitor | | drain junction) |
| 304 | Storage node isolated by | 323 | (E) With means to facilitate |
| 30 1 | dielectric from semiconductor | 323 | light erasure |
| | substrate | 324 | (E) Multiple insulator layers |
| 305 | With means to insulate | 321 | (e.g., MNOS structure) |
| 303 | adjacent storage nodes (e.g., | 325 | (E) Non-homogeneous |
| | channel stops or field oxide) | 323 | composition insulator layer |
| 306 | Stacked capacitor | | (e.g., graded composition |
| 307 | Parallel interleaved | | |
| 307 | | | layer or layer with inclusions) |
| | <pre>capacitor electrode pairs (e.g., interdigitized)</pre> | 326 | With additional, non-memory |
| 308 | With capacitor electrodes | 320 | control electrode or channel |
| 300 | connection portion located | | portion (e.g., accessing field |
| | _ | | effect transistor structure) |
| | centrally thereof (e.g., fin | 207 | , |
| 200 | electrodes with central post) | 327 | Short channel insulated gate |
| 309 | With increased effective | 200 | field effect transistor |
| | electrode surface area (e.g., | 328 | Vertical channel or double |
| | tortuous path, corrugated, or textured electrodes) | | diffused insulated gate field |
| 210 | , , , , , , , , , , , , , , , , , , , , | | effect device provided with |
| 310 | With high dielectric constant | | means to protect against |
| 211 | insulator (e.g., Ta20s) | | excess voltage (e.g., gate |
| 311 | Storage node isolated by | 200 | protection diode) |
| | dielectric from semiconductor | 329 | (E) Gate controls vertical |
| | substrate | | charge flow portion of channel |
| 312 | Voltage variable capacitor | | (e.g., VMOS device) |
| | (i.e., capacitance varies with | 330 | (E) Gate electrode in groove |
| | applied voltage) | 331 | Plural gate electrodes or |
| 313 | Inversion layer capacitor | | grid shaped gate electrode |
| 314 | Variable threshold (e.g., | 332 | Gate electrode self-aligned |
| | floating gate memory device) | | with groove |
| 315 | \dots (E) With floating gate | 333 | With thick insulator to |
| | electrode | | reduce gate capacitance in |
| 316 | With additional contacted | | non-channel areas (e.g., thick |
| | control electrode | | oxide over source or drain |
| | | | region) |
| | | | |

| 334 | (E) In integrated circuit | 351 | (E) Complementary field |
|-----|-------------------------------------------------------------|-----|---------------------------------------------------------|
| | structure | | effect transistor structures |
| 335 | \dots (E) Active channel region has | | only (i.e., not including |
| | a graded dopant concentration | | bipolar transistors, |
| | decreasing with distance from | | resistors, or other |
| | source region (e.g., double | 250 | components) |
| | diffused device, DMOS transistor) | 352 | (E) Substrate is single |
| 336 | , | | <pre>crystal insulator (e.g., sapphire or spinel)</pre> |
| 330 | With lightly doped portion of drain region adjacent channel | 353 | Single crystal islands or |
| | (e.g., LDD structure) | 333 | semiconductor layer containing |
| 337 | (E) In integrated circuit | | only one active device |
| 337 | structure | 354 | Including means to eliminate |
| 338 | (E) With complementary field | | island edge effects (e.g., |
| | effect transistor | | insulating filling between |
| 339 | With means to increase | | islands, or channel stop |
| | breakdown voltage | | regions in island edges) |
| 340 | With means (other than self- | 355 | (E) With overvoltage protective |
| | alignment of the gate | | means |
| | electrode) to decrease gate | 356 | \dots (E) For protecting against |
| | capacitance (e.g., shield | | gate insulator breakdown |
| | electrode) | 357 | (E) In complementary field |
| 341 | (E) Plural sections connected | | effect transistor integrated |
| | in parallel (e.g., power | 250 | circuit |
| 240 | MOSFET) | 358 | (E) Including resistor element |
| 342 | (E) With means to reduce ON resistance | 359 | (E) As thin film structure |
| 343 | (E) All contacts on same | 339 | (e.g., polysilicon resistor) |
| 343 | surface (e.g., lateral | 360 | (E) Protection device |
| | structure) | 300 | includes insulated gate |
| 344 | (E) With lightly doped portion | | transistor structure (e.g., |
| 311 | of drain region adjacent | | combined with resistor |
| | channel (e.g., LDD structure) | | element) |
| 345 | With means to prevent sub- | 361 | (E) For operation as bipolar |
| | surface currents, or with non- | | or punchthrough element |
| | uniform channel doping | 362 | \ldots (E) Punchthrough or bipolar |
| 346 | Gate electrode overlaps the | | element |
| | source or drain by no more | 363 | (E) Including resistor |
| | than depth of source or drain | | element |
| | (e.g., self-aligned gate) | 364 | (\mathtt{E}) With resistive gate |
| 347 | (E) Single crystal | 265 | electrode |
| | semiconductor layer on | 365 | (E) With plural, separately |
| 240 | insulating substrate (SOI) | | connected, gate electrodes in |
| 348 | (E) Depletion mode field effect transistor | 266 | same device |
| 349 | With means (e.g., a buried | 366 | (E) Overlapping gate electrodes |
| 317 | channel stop layer) to prevent | 367 | (E) Insulated gate controlled |
| | leakage current along the | 307 | breakdown of pn junction |
| | interface of the semiconductor | | (e.g., field plate diode) |
| | layer and the insulating | 368 | (E) Insulated gate field effect |
| | substrate | - | transistor in integrated |
| 350 | \dots (E) Insulated electrode device | | circuit |
| | is combined with diverse type | 369 | (E) Complementary insulated |
| | device (e.g., complementary | | gate field effect transistors |
| | MOSFETs, FET with resistor, | | |
| | etc.) | | |

257 - 10 CLASS 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE DIODES)

| 370 | (E) Combined with bipolar transistor | 386 | With means to reduce parasitic capacitance |
|-----|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| 371 | <pre>(E) Complementary transistors in wells of opposite conductivity types more heavily doped than the substrate region in which they are formed, e.g., twin wells</pre> | 387 | Gate electrode overlaps at least one of source or drain by no more than depth of source or drain (e.g., selfaligned gate) |
| 372 | With means to prevent latchup or parasitic conduction | | refractory or platinum group metal or silicide |
| 272 | channels | 389 | With thick insulator over |
| 373 | With pn junction to collect injected minority carriers to prevent parasitic bipolar transistor action | 390 | source or drain region(E) Matrix or array of field effect transistors (e.g., array of FETs only some of |
| 374 | <pre>Dielectric isolation means (e.g., dielectric layer in vertical grooves)</pre> | | <pre>which are completed, or structure for mask programmed read-only memory (ROM))</pre> |
| 375 | <pre>With means to reduce substrate spreading resistance (e.g., heavily doped substrate)</pre> | 391 | <pre>(E) Selected groups of complete field effect devices having different threshold voltages (e.g., different</pre> |
| 376 | With barrier region of reduced minority carrier lifetime (e.g., heavily doped P+ region to reduce electron minority carrier lifetime, or containing deep level impurity or crystal damage), or with | 392 | channel dopant concentrations)(E) Insulated gate field effect transistors of different threshold voltages in same integrated circuit (e.g., enhancement and depletion mode) |
| 377 | region of high threshold voltage (e.g., heavily doped channel stop region)With polysilicon interconnections to source or | 393 | (E) Insulated gate field effect transistor adapted to function as load element for switching insulated gate field effect transistor |
| | drain regions (e.g., | 394 | With means to prevent |
| | polysilicon laminated with | 394 | parasitic conduction channels |
| | silicide) | 395 | Thick insulator portion |
| 378 | (E) Combined with bipolar transistor | 396 | Recessed into semiconductor surface |
| 379 | (E) Combined with passive | 397 | In vertical-walled groove |
| | components (e.g., resistors) | 398 | Combined with heavily doped |
| 380 | (E) Polysilicon resistor | | channel stop portion |
| 381 | <pre>(E) With multiple levels of polycrystalline silicon</pre> | 399 | Combined with heavily doped channel stop portion |
| 382 | <pre>(E) With contact to source or drain region of refractory</pre> | 400 | With heavily doped channel stop portion |
| | <pre>material (e.g., polysilicon, tungsten, or silicide)</pre> | 401 | <pre>(E) With specified physical layout (e.g., ring gate,</pre> |
| 383 | <pre>(E) Contact of refractory or platinum group metal (e.g., molybdenum, tungsten, or titanium)</pre> | | source/drain regions shared between plural FETs, plural sections connected in parallel to form power MOSFET) |
| 384 | (E) Including silicide | 402 | With permanent threshold |
| 385 | (E) Multiple polysilicon layers | | adjustment (e.g., depletion mode) |

| 403 | With channel conductivity dopant same type as that of source and drain | 424 | Sensor with region of high carrier recombination (e.g., magnetodiode with carriers |
|-----|-------------------------------------------------------------------------|------|------------------------------------------------------------------------------------|
| 404 | Non-uniform channel doping | | deflected to recombination |
| 405 | With gate insulator containing specified permanent charge | 425 | region by magnetic field)Magnetic field detector using |
| 406 | Plural gate insulator layers | | compound semiconductor |
| 407 | With gate electrode of controlled workfunction | | <pre>material (e.g., GaAs, InSb, etc.)</pre> |
| | material (e.g., low | 426 | Differential output (e.g., with |
| | workfunction gate material) | | offset ajustment means or with |
| 408 | <pre>(E) Including lightly doped drain portion adjacent channel</pre> | | <pre>means to reduce temperature sensitivity)</pre> |
| | (e.g., lightly doped drain, | 427 | (E) Magnetic field sensor in |
| | LDD device) | | integrated circuit (e.g., in |
| 409 | With means to increase | | bipolar transistor integrated |
| | breakdown voltage (e.g., field | 400 | circuit) |
| | <pre>shield electrode, guard ring, etc.)</pre> | 428 | <pre>.(E) Electromagnetic or particle radiation</pre> |
| 410 | Gate insulator includes | 429 | (E) Charged or elementary |
| | material (including air or | | particles |
| | vacuum) other than SiO2 | 430 | (E) With active region having |
| 411 | Composite or layered gate | | effective impurity concentration less than 1012 |
| | <pre>insulator (e.g., mixture such as silicon oxynitride)</pre> | | atoms/cm3 |
| 412 | Gate electrode of refractory | 431 | (E) Light |
| | material (e.g., polysilicon or | 432 | (E) With optical element |
| | silicide of a refractory or platinum group metal) | 433 | (E) With housing or encapsulation |
| 413 | Polysilicon laminated with | 434 | (E) With window means |
| 413 | silicide | 435 | (E) With optical shield or |
| 414 | RESPONSIVE TO NON-ELECTRICAL | 155 | mask means |
| | SIGNAL (E.G., CHEMICAL, | 436 | (E) With means for increasing |
| | STRESS, LIGHT, OR MAGNETIC | | light absorption (e.g., |
| | FIELD SENSORS) | | redirection of unabsorbed |
| 415 | .(E) Physical deformation | | light) |
| 416 | Acoustic wave | 437 | \dots (E) Antireflection coating |
| 417 | Strain sensors | 438 | \dots (E) Avalanche junction |
| 418 | With means to concentrate | 439 | (E) Containing dopant adapted |
| 410 | stress | 4.40 | for photoionization |
| 419 | With thinned central active portion of semiconductor | 440 | (E) With different sensor |
| | surrounded by thick | | <pre>portions responsive to different wavelengths (e.g.,</pre> |
| | insensitive portion (e.g., | | color imager) |
| | diaphragm type strain gauge) | 441 | (E) Narrow band gap |
| 420 | Means to reduce sensitivity to | | semiconductor () (e.g., |
| | physical deformation | | PbSnTe) |
| 421 | .(E) Magnetic field | 442 | (E) II-VI compound |
| 422 | With magnetic field directing | | <pre>semiconductor (e.g., HgCdTe)</pre> |
| | <pre>means (e.g., shield, pole piece, etc.)</pre> | 443 | <pre>(E) Matrix or array (e.g., single line arrays)</pre> |
| 423 | Bipolar transistor magnetic | | • • |
| | field sensor (e.g., lateral bipolar transistor) | | |

257 - 12 CLASS 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE DIODES)

| 444 | <pre>(E) Light sensor elements overlie active switching</pre> | 463 | (E) With particular doping concentration |
|------|-----------------------------------------------------------------|-------|-------------------------------------------------------------|
| | elements in integrated circuit | 1.6.1 | |
| | 3 | 464 | (E) With particular layer |
| | (e.g., where the sensor | | thickness (e.g., layer less |
| | elements are deposited on an | | than light absorption depth) |
| | integrated circuit) | 465 | \dots (E) Geometric configuration |
| 445 | \dots (E) With antiblooming means | | of junction (e.g., fingers) |
| 446 | \dots (E) With specific isolation | 466 | (E) External physical |
| | means in intgrated circuit | | configuration of semiconductor |
| 447 | (E) With backside | | (e.g., mesas, grooves) |
| | illumination (e.g., having a | 467 | .Temperature |
| | thinned central area or a non- | 468 | Semiconductor device operated |
| | absorbing substrate) | | at cryogenic temperature |
| 448 | (E) With particular electrode | 469 | With means to reduce |
| | configuration | 100 | temperature sensitivity (e.g., |
| 449 | (E) Schottky barrier (e.g., a | | reduction of temperature |
| | transparent Schottky metallic | | sensitivity of junction |
| | layer or a Schottky barrier | | |
| | containing at least one of | | breakdown voltage by using a compensating element) |
| | indium or tin (e.g., SnO2, | 470 | Pn junction adapted as |
| | indium tin oxide)) | 2.0 | temperature sensor |
| 450 | \dots (E) With doping profile to | 471 | (E) SCHOTTKY BARRIER |
| | adjust barrier height | 472 | .(E) To compound semiconductor |
| 451 | (E) Responsive to light | 473 | With specified Schottky metal |
| | having lower energy (i.e., | 474 | .As active junction in bipolar |
| | longer wavelength) than | 1,1 | transistor (e.g., Schottky |
| | forbidden band gap energy of | | collector) |
| | semiconductor (e.g., by | 475 | .With doping profile to adjust |
| | excitation of carriers from | 175 | barrier height |
| | metal into semiconductor) | 476 | _ |
| 452 | (E) With edge protection, | | .(E) In integrated structure |
| | e.g., doped guard ring or mesa | 477 | (E) With bipolar transistor |
| | structure | 478 | (E) Plural Schottky barriers with different barrier heights |
| 453 | (E) With specified Schottky | 479 | (E) Connected across base- |
| | metallic layer | 4/9 | , , , , , , , , , , , , , , , , , , , , |
| 454 | (E) Schottky metallic layer | | collector junction of |
| | is a silicide | 400 | transistor (e.g., Baker clamp) |
| 455 | (E) Silicide of Platinum | 480 | .In voltage variable capacitance |
| | group metal | 401 | diode |
| 456 | (E) Silicide of refractory | 481 | .(E) Avalanche diode (e.g., so- |
| | metal | | called "Zener" diode having |
| 457 | (E) With particular contact | | breakdown voltage greater than |
| 13, | geometry (e.g., ring or grid) | | 6 volts) |
| 458 | (E) PIN detector, including | 482 | (E) Microwave transit time |
| 430 | combinations with non-light | | device (e.g., IMPATT diode) |
| | responsive active devices | 483 | .With means to prevent edge |
| 459 | (E) With particular contact | | breakdown |
| 439 | geometry (e.g., ring or grid, | 484 | (E) Guard ring |
| | | 485 | .Specified materials |
| 160 | or bonding pad arrangement) | 486 | Layered (e.g., a diffusion |
| 460 | (E) With backside illumination | | barrier material layer or a |
| | (e.g., with a thinned central | | silicide layer or a precious |
| | area or non-absorbing | | metal layer) |
| 1.61 | substrate) | 487 | (E) WITH MEANS TO INCREASE |
| 461 | (E) Light responsive pn | | BREAKDOWN VOLTAGE THRESHOLD |
| 460 | junction | 488 | .(E) Field relief electrode |
| 462 | (E) Phototransistor | | |

| 489 | Resistive | 503 | .(E) With contact or |
|-----|------------------------------------------------------------------------|-------|----------------------------------------------------------|
| 490 | Combined with floating pn junction guard region | | metallization configuration to reduce parasitic coupling |
| 491 | .(E) In integrated circuit | | (e.g., separate ground pads |
| 492 | (E) With electric field | | for different parts of |
| | controlling semiconductor | | integrated circuit) |
| | layer having a low enough | 504 | .(E) Including means for |
| | doping level in relationship | | establishing a depletion |
| | to its thickness to be fully | | region throughout a semi- |
| | depleted prior to avalanche | | conductor layer for isolating |
| | breakdown (e.g., RESURF | | devices in different portions |
| | devices) | | of the layer (e.g., "JFET" |
| 493 | .With electric field controlling | | isolation) |
| | semiconductor layer having a | 505 | .(E) With polycrystalline |
| | low enough doping level in | | semiconductor isolation region |
| | relationship to its thickness | | in direct contact with single |
| | to be fully depleted prior to | | crystal active semiconductor |
| | avalanche breakdown (e.g., | | material |
| | RESURF devices) | 506 | .(E) Including dielectric |
| 494 | .Reverse-biased pn junction guard | 505 | isolation means |
| | region | 507 | (E) With single crystal |
| 495 | .Floating pn junction guard | | <pre>insulating substrate (e.g., sapphire)</pre> |
| 100 | region | 508 | (E) With metallic conductor |
| 496 | .With physical configuration of | 300 | within isolating dielectric or |
| | semiconductor surface to | | between semiconductor and |
| | <pre>reduce electric field (e.g., reverse bevels, double bevels,</pre> | | isolating dielectric (e.g., |
| | stepped mesas, etc.) | | metal shield layer or internal |
| 497 | (E) PUNCHTHROUGH STRUCTURE DEVICE | | connection layer) |
| 107 | (E.G., PUNCHTHROUGH | 509 | (E) Combined with pn junction |
| | TRANSISTOR, CAMEL BARRIER | | isolation (e.g., isoplanar, |
| | DIODE) | | LOCOS) |
| 498 | .(E) Punchthrough region fully | 510 | (E) Dielectric in groove |
| | depleted at zero external | 511 | (E) With complementary (npn |
| | applied bias voltage (e.g., | | and pnp) bipolar transistor |
| | camel barrier or planar doped | | structures) |
| | barrier devices, or so-called | 512 | (E) Complementary devices |
| | "bipolar SIT" devices) | | share common active region |
| 499 | (E) INTEGRATED CIRCUIT STRUCTURE | | (e.g., integrated injection |
| | WITH ELECTRICALLY ISOLATED | | logic, I2L) |
| | COMPONENTS | 513 | \dots (E) Vertical walled groove |
| 500 | .(E) Including high voltage or | 514 | \dots (E) With active junction |
| | high power devices isolated | | abutting groove (e.g., "walled |
| | from low voltage or low power | | emitter") |
| | devices in the same integrated | 515 | \dots (E) With active junction |
| | circuit | | abutting groove (e.g., "walled |
| 501 | (E) Including dielectric | | emitter") |
| | isolation means | 516 | \dots (E) With passive component |
| 502 | (E) High power or high voltage | | (e.g., resistor, capacitor, |
| | device extends completely | | etc.) |
| | through semiconductor | 517 | \dots (E) With bipolar transistor |
| | substrate (e.g., backside | = 4 - | structure |
| | collector contact) | 518 | (E) With polycrystalline |
| | | | connecting region (e.g., |
| | | | polysilicon base contact) |

257 - 14 CLASS 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE DIODES)

| 519 | <pre>(E) Including heavily doped channel stop region adjacent groove</pre> | 542 | <pre>(E) Resistor has same doping as emitter or collector of bipolar transistor</pre> |
|-------|--------------------------------------------------------------------------------|-------|-------------------------------------------------------------------------------------------|
| 520 | (E) Conductive filling in dielectric-lined groove (e.g., polysilicon backfill) | 543 | (E) Lightly doped junction isolated resistor (e.g., ion implanted resistor) |
| 521 | (E) Sides of grooves along | 544 | .(E) With pn junction isolation |
| 221 | | | |
| | major crystal planes (e.g., | 545 | (\mathtt{E}) With means to control |
| | (111), (100) planes, etc.) | | isolation junction capacitance |
| 522 | (E) Air isolation (e.g., beam | | (e.g., lightly doped layer at |
| 0 | lead supported semiconductor | | isolation junction to increase |
| | | | |
| | islands) | | depletion layer width) |
| 523 | (\mathtt{E}) Isolation by region of | 546 | \ldots (E) With structural means to |
| | intrinsic (undoped) | | protect against excess or |
| | semiconductor material (e.g., | | reversed polarity voltage |
| | including region physically | 547 | (E) With structural means to |
| | | 347 | ` ' |
| | damaged by proton bombardment) | | control parasitic transistor |
| 524 | (E) Full dielectric isolation | | action or leakage current |
| | with polycrystalline | 548 | (\mathtt{E}) At least three regions of |
| | semiconductor substrate | | alternating conductivity types |
| 525 | (E) With complementary (npn | | with dopant concentration |
| 323 | | | gradients decreasing from |
| | and pnp) bipolar transistor | | surface of semiconductor |
| | structures | | |
| 526 | (E) With bipolar transistor | | (e.g., "triple-diffused" |
| | structure | | integrated circuit) |
| 527 | (E) Sides of isolated | 549 | (E) With substrate and lightly |
| | semiconductor islands along | | doped surface layer of same |
| | _ | | conductivity type, separated |
| | major crystal planes (e.g., | | |
| | (111), (100) planes, etc.) | | by subsurface heavily doped |
| 528 | .(E) Passive components in ICs | | region of opposite |
| 529 | (E) Including programmable | | conductivity type (e.g., |
| | passive component (e.g., fuse) | | "collector diffused isolation" |
| 530 | (E) Anti-fuse | | integrated circuit) |
| 531 | (E) Including inductive element | 550 | (E) With lightly doped surface |
| | _ | | layer of one conductivity type |
| 532 | (E) Including capacitor | | on substrate of opposite |
| | component | | conductivity type, having |
| 533 | (E) Combined with resistor to | | |
| | form RC filter structure | | plural heavily doped portions |
| 534 | (E) With means to increase | | of the one conductivity type |
| 331 | | | between the layer and |
| | surface area (e.g., grooves, | | substrate, different ones of |
| | ridges, etc.) | | the heavily doped portions |
| 535 | (E) Both terminals of | | having differing depths or |
| | capacitor isolated from | | physical extent |
| | substrate | F F 1 | |
| 536 | (E) Including resistive element | 551 | (E) Including voltage reference |
| | | | element (e.g., avalanche |
| 537 | (E) Using specific resistive | | diode, so-called "Zener diode" |
| | material | | with breakdown voltage greater |
| 538 | (E) Polycrystalline silicon | | than 6 volts or with positive |
| | (doped or undoped) | | temperature coefficient of |
| 539 | (E) Combined with bipolar | | breakdown voltage) |
| | transistor | EEO | |
| E 4 O | | 552 | (E) With bipolar transistor |
| 540 | (E) With compensation for | | structure |
| | non-linearity (e.g., dynamic | | |
| | isolation pocket bias) | | |
| 541 | (E) Pinch resistor | | |
| | | | |

| 554(E) With connecting region to input transition made of polycrystalline emitter junction semiconductor material (e.g., polysilicon base contact) (e.g., mesa emitter junction semiconductor material (e.g., polysilicon base contact) (e.g., mesa emitter junction semiconductor material (e.g., polysilicon base contact) (e.g., mesa emitter junction semiconductor material (e.g., polysilicon polysilicon polysilicon polysilicon polysilicon semiconductor material (e.g., polysilicon poly | re components in rlington .g., diode, bleeder d antiparallel |
|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------|
| transistor structures (e.g., 572(E) With resist | on, etc.) |
| I2L) base regions | tance means |
| 556(E) Including lateral bipolar 573(E) With housing transistor structure structure or co | |
| 557 .(E) Lateral bipolar transistor 574(E) Complementa: structure share common ac | |
| 558(E) With base region doping (e.g., integrat concentration step or gradient logic I2L) | ted injection |
| or with means to increase 575(E) Including transistor structures 575 or 575 | |
| 559(E) With active region formed 576(E) With contact along groove or exposed edge refractory material response refractory material refractory material response refractory material response response refractory material response r | erial (e.g., |
| 560(E) With multiple collectors or refractory or pemitters metal) | platinum group |
| 561(E) With different emitter to 577 .(E) Including add component in sa areas isolated struct | ame, non- ture (e.g., |
| 562(E) With auxiliary collector/ transistor with re-emitter between emitter and output collector (e.g., etc.) | h resistor, |
| "current hogging logic" 578 .(E) With enlarged device) (e.g., power de | |
| 563 .(E) With multiple separately 579(E) With separate connected emitter, collector, connected in page 579 | |
| or base regions in same 580(E) With current transistor structure means (e.g., end ballasting residue) | mitter |
| regions current ballast | ting means) |
| 565 (E) BIPOLAR TRANSISTOR STRUCTURE 581(E) Thin film 566 .(E) Plural non-isolated means (e.g., po | |
| transistor structures in same resistor) structure 582(E) With current | t ballasting |
| | istors or base ting resistors) |
| · · · · · · · · · · · · · · · · · · · | cted portions of |
| 568(E) More than two Darlington- transistor (e.g. connected transistors region doping to the connected transistors | under central |
| 569(E) Complementary Darlington- connected transistors 584With housing or electrode) mean | kdown) contact (i.e., |

257 - 16 CLASS 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE DIODES)

| 585 | .With means to increase inverse | 604 | <pre>.(E) Microwave transit time device (e.g., IMPATT diode)</pre> |
|------------|----------------------------------------------------------|-----|----------------------------------------------------------------------|
| 586 | .(E) With non-planar | 605 | .(E) With means to limit area of |
| | semiconductor surface (e.g., | | breakdown (e.g., guard ring |
| | groove, mesa, bevel, etc.) | | having higher breakdown |
| 587 | .With specified electrode means | | voltage) |
| 588 | Including polycrystalline | 606 | Subsurface breakdown |
| 300 | semiconductor as connection | 607 | WITH SPECIFIED DOPANT (E.G., |
| 589 | .(E) Avalanche transistor | 007 | PLURAL DOPANTS OF SAME |
| 590 | .(E) Avaianche transistor .With means to reduce minority | | CONDUCTIVITY IN SAME REGION) |
| 590 | carrier lifetime (e.g., region | 608 | .Switching device based on |
| | , , , , | 000 | filling and emptying of deep |
| | of deep level dopant or region | | energy levels |
| F 0 1 | of crystal damage) | 609 | |
| 591 | .With emitter region having | 609 | .For compound semiconductor |
| | specified doping concentration | C10 | (e.g., deep level dopant) |
| | profile (e.g., high-low | 610 | .Deep level dopant |
| | concentration step) | 611 | With specified distribution |
| 592 | .(E) With base region having | | (e.g., laterally localized, |
| | specified doping concentration | | with specified concentration |
| | profile or specified | | distribution or gradient) |
| | configuration (e.g., inactive | 612 | Deep level dopant other than |
| | base more heavily doped than | | gold or platinum |
| | active base or base region has | 613 | (E) INCLUDING SEMICONDUCTOR |
| | constant doping concentration | | MATERIAL OTHER THAN SILICON OR |
| | portion (e.g., epitaxial | | GALLIUM ARSENIDE (GAAS) (E.G., |
| 500 | base)) | | PBXSN1-XTE) |
| 593 | .With means to increase current | 614 | .(E) Group II-VI compound (e.g., |
| | gain or operating frequency | | CdTe, HgxCd1-xTe) |
| 594 | (E) WITH GROOVE TO DEFINE PLURAL | 615 | .(E) Group III-V compound (e.g., |
| | DIODES | | InP) |
| 595 | (E) VOLTAGE VARIABLE CAPACITANCE | 616 | .(E) Containing germanium, Ge |
| | DEVICE | 617 | (E) INCLUDING REGION CONTAINING |
| 596 | .With specified dopant profile | | CRYSTAL DAMAGE |
| 597 | Retrograde dopant profile | 618 | (E) PHYSICAL CONFIGURATION OF |
| | (e.g., dopant concentration | | SEMICONDUCTOR (E.G., MESA, |
| | decreases with distance from | | BEVEL, GROOVE, ETC.) |
| | rectifying junction) | 619 | .With thin active central |
| 598 | .With plural junctions whose | | semiconductor portion |
| | depletion regions merge to | | surrounded by thicker inactive |
| | vary voltage dependence | | shoulder (e.g., for mechanical |
| 599 | .With means to increase active | | support) |
| | junction area (e.g., grooved | 620 | .With peripheral feature due to |
| | or convoluted surface) | | separation of smaller |
| 600 | .With physical configuration to | | semiconductor chip from larger |
| | vary voltage dependence (e.g., | | wafer (e.g., scribe region, or |
| | mesa) | | means to prevent edge effects |
| 601 | .(E) Plural diodes in same non- | | such as leakage current at |
| | isolated structure, or device | | peripheral chip separation |
| | having three or more terminals | | area) |
| 602 | .With specified housing or | 621 | .With electrical contact in hole |
| | contact | | in semiconductor (e.g., lead |
| 603 | (E) AVALANCHE DIODE (E.G., SO- | | extends through semiconductor |
| | CALLED "ZENER" DIODE HAVING | | body) |
| | BREAKDOWN VOLTAGE GREATER THAN | 622 | .Groove |
| | 6 VOLTS) | | |
| | | | |

| 623 | <pre>.(E) Mesa structure (e.g., including undercut or stepped</pre> | 640 | At least one layer of silicon nitride |
|-------------------------------------------------------------|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| | mesa configuration or having | 641 | Combined with glass layer |
| 624 | constant slope taper) | 642 | (E) At least one layer of |
| 024 | With low resistance ohmic | - 40 | organic material |
| | connection means along exposed | 643 | \dots (E) Polyimide or polyamide |
| | mesa edge (e.g., contact or | 644 | At least one layer of glass |
| | heavily doped region along | 645 | Insulating layer containing |
| | exposed mesa to reduce "skin effect" losses in microwave | | specified electrical charge |
| | diode) | | <pre>(e.g., net negative electrical charge)</pre> |
| 625 | Semiconductor body including | 646 | Coating of semi-insulating |
| | mesa is intimately bonded to | | material (e.g., amorphous |
| | thick electrical and/or | | silicon or silicon-rich |
| | thermal conductor member of | | silicon oxide) |
| | larger lateral extent than | 647 | Insulating layer recessed into |
| | semiconductor body (e.g., | | semiconductor surface (e.g., |
| | "plated heat sink" microwave | | LOCOS oxide) |
| | diode) | 648 | Combined with channel stop |
| 626 | Combined with passivating | | region in semiconductor |
| 600 | coating | 649 | Insulating layer of silicon |
| 627 | .(E) With specified crystal plane | | nitride or silicon oxynitride |
| 600 | or axis | 650 | Insulating layer of glass |
| 628 | Major crystal plane or axis | 651 | Details of insulating layer |
| | other than (100), (110), or | | electrical charge (e.g., |
| | (111) (e.g., (731) axis, crystal plane several degrees | | negative insulator layer |
| | from (100) toward (011), etc.) | | charge) |
| | IIOm (IOO) cowara (OII), ccc.) | 652 | (D) |
| 629 | (E) WITH MEANS TO CONTROL SURFACE | | .(E) Channel stop layer |
| 629 | (E) WITH MEANS TO CONTROL SURFACE | 653 | (E) Channel stop layer (E) WITH SPECIFIED SHAPE OF PN |
| | EFFECTS | 653 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION |
| 630 | | | (E) WITH SPECIFIED SHAPE OF PN JUNCTION .Interdigitated pn junction or |
| | <pre>EFFECTS .(E) With inversion-preventing shield electrode</pre> | 653 | <pre>(E) WITH SPECIFIED SHAPE OF PN JUNCTION .Interdigitated pn junction or more heavily doped side of</pre> |
| 630 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor</pre> | 653 654 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION .Interdigitated pn junction or more heavily doped side of junction is concave |
| 630 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs)</pre> | 653 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION .Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY |
| 630 631 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating</pre> | 653 654 655 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION .Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT |
| 630 631 632 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coatingWith thermal expansion</pre> | 653 654 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION .Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT .With high resistivity (e.g., |
| 630 631 632 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coatingWith thermal expansion compensation (e.g., thermal</pre> | 653 654 655 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION .Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT .With high resistivity (e.g., "intrinsic") layer between p |
| 630 631 632 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coatingWith thermal expansion</pre> | 653 654 655 656 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION .Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT .With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) |
| 630 631 632 | EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant | 653 654 655 656 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT .With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) .Stepped profile |
| 630 631 632 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of</pre> | 653 654 655 656 657 658 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY |
| 630 631 632 633 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coatingWith thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor)</pre> | 653 654 655 656 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., |
| 630 631 632 633 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coatingWith thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor)Insulating coating of glass</pre> | 653 654 655 656 657 658 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC |
| 630 631 632 633 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor)Insulating coating of glass composition containing</pre> | 653 654 655 656 657 658 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM |
| 630 631 632 633 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor)Insulating coating of glass composition containing component to adjust melting or</pre> | 653 654 655 656 657 658 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM ELECTROMAGNETIC RADIATION OR |
| 630 631 632 633 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor)Insulating coating of glass composition containing component to adjust melting or softening temperature (e.g.,</pre> | 653 654 655 656 657 658 659 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM ELECTROMAGNETIC RADIATION OR CHARGE PARTICLES) |
| 630 631 632 633 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor)Insulating coating of glass composition containing component to adjust melting or softening temperature (e.g., low melting point glass)</pre> | 653 654 655 656 657 658 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM ELECTROMAGNETIC RADIATION OR CHARGE PARTICLES) .(E) With means to shield device |
| 630 631 632 633 | <pre>EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coatingWith thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor)Insulating coating of glass composition containing component to adjust melting or softening temperature (e.g., low melting point glass)Multiple layers</pre> | 653 654 655 656 657 658 659 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM ELECTROMAGNETIC RADIATION OR CHARGE PARTICLES) .(E) With means to shield device contained in housing or |
| 630 631 632 633 | EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor) .Insulating coating of glass composition containing component to adjust melting or softening temperature (e.g., low melting point glass) .Multiple layers At least one layer of seminisulating material Three or more insulating | 653 654 655 656 657 658 659 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM ELECTROMAGNETIC RADIATION OR CHARGE PARTICLES) .(E) With means to shield device contained in housing or package from charged particles |
| 630 631 632 633 634 635 636 637 | EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor) .Insulating coating of glass composition containing component to adjust melting or softening temperature (e.g., low melting point glass) .Multiple layers At least one layer of seminisulating material Three or more insulating layers | 653 654 655 656 657 658 659 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM ELECTROMAGNETIC RADIATION OR CHARGE PARTICLES) .(E) With means to shield device contained in housing or package from charged particles (e.g., alpha particles) or |
| 630 631 632 633 634 | EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor) Insulating coating of glass composition containing component to adjust melting or softening temperature (e.g., low melting point glass) Multiple layers At least one layer of seminisulating material Three or more insulating layers With discontinuous or varying | 653 654 655 656 657 658 659 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM ELECTROMAGNETIC RADIATION OR CHARGE PARTICLES) .(E) With means to shield device contained in housing or package from charged particles |
| 630 631 632 633 634 635 636 637 | EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor) .Insulating coating of glass composition containing component to adjust melting or softening temperature (e.g., low melting point glass) .Multiple layersAt least one layer of semi-insulating materialThree or more insulating layersWith discontinuous or varying thickness layer (e.g., layer | 653 654 655 656 657 658 659 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM ELECTROMAGNETIC RADIATION OR CHARGE PARTICLES) .(E) With means to shield device contained in housing or package from charged particles (e.g., alpha particles) or highly ionizing radiation |
| 630 631 632 633 634 635 636 637 | EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor)Insulating coating of glass composition containing component to adjust melting or softening temperature (e.g., low melting point glass)Multiple layersAt least one layer of semi-insulating materialThree or more insulating layersWith discontinuous or varying thickness layer (e.g., layer covers only selected portions | 653 654 655 656 657 658 659 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM ELECTROMAGNETIC RADIATION OR CHARGE PARTICLES) .(E) With means to shield device contained in housing or package from charged particles (e.g., alpha particles) or highly ionizing radiation (i.e., hard X-rays or shorter |
| 630 631 632 633 634 635 636 637 638 | EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coatingWith thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor)Insulating coating of glass composition containing component to adjust melting or softening temperature (e.g., low melting point glass)Multiple layersAt least one layer of seminisulating materialThree or more insulating layersWith discontinuous or varying thickness layer (e.g., layer covers only selected portions of semiconductor) | 653 654 655 656 657 658 659 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM ELECTROMAGNETIC RADIATION OR CHARGE PARTICLES) .(E) With means to shield device contained in housing or package from charged particles (e.g., alpha particles) or highly ionizing radiation (i.e., hard X-rays or shorter wavelength) |
| 630 631 632 633 634 635 636 637 | EFFECTS .(E) With inversion-preventing shield electrode .In compound semiconductor material (e.g., GaAs) .(E) Insulating coating .With thermal expansion compensation (e.g., thermal expansion of glass passivant matched to that of semiconductor)Insulating coating of glass composition containing component to adjust melting or softening temperature (e.g., low melting point glass)Multiple layersAt least one layer of semi-insulating materialThree or more insulating layersWith discontinuous or varying thickness layer (e.g., layer covers only selected portions | 653 654 655 656 657 658 659 | (E) WITH SPECIFIED SHAPE OF PN JUNCTION Interdigitated pn junction or more heavily doped side of junction is concave WITH SPECIFIED IMPURITY CONCENTRATION GRADIENT With high resistivity (e.g., "intrinsic") layer between p and n layers (e.g., PIN diode) Stepped profile PLATE TYPE RECTIFIER ARRAY (E) WITH SHIELDING (E.G., ELECTRICAL OR MAGNETIC SHIELDING, OR FROM ELECTROMAGNETIC RADIATION OR CHARGE PARTICLES) (E) With means to shield device contained in housing or package from charged particles (e.g., alpha particles) or highly ionizing radiation (i.e., hard X-rays or shorter wavelength) (E) SUPERCONDUCTIVE CONTACT OR |

257 - 18 CLASS 257 ACTIVE SOLID-STATE DEVICES (E.G., TRANSISTORS, SOLID-STATE DIODES)

| 663 | 0 | 689 | Divid alastuada wantian |
|-----|----------------------------------------------------|-----|---------------------------------|
| 664 | .On integrated circuit | 690 | Rigid electrode portion |
| 004 | TRANSMISSION LINE LEAD (E.G., | 691 | .With contact or lead |
| 665 | STRIPLINE, COAX, ETC.) CONTACTS OR LEADS INCLUDING | 691 | Having power distribution means |
| 005 | | 692 | (e.g., bus structure) |
| | FUSIBLE LINK MEANS OR NOISE SUPPRESSION MEANS | 693 | With particular lead geometry |
| 666 | LEAD FRAME | | External connection to housing |
| 667 | | 694 | Axial leads |
| 668 | .With dam or vent for encapsulant | 695 | Fanned/radial leads |
| 008 | .On insulating carrier other than | 696 | Bent (e.g., J-shaped) lead |
| | a printed circuit board | 697 | Pin grid type |
| 669 | .With stress relief | 698 | With specific electrical |
| 670 | .With separate tie bar element or | | feedthrough structure |
| | plural tie bars | 699 | Housing entirely of metal |
| 671 | Of insulating material | | except for feedthrough |
| 672 | .Small lead frame (e.g., "spider" | | structure |
| | frame) for connecting a large | 700 | Multiple contact layers |
| | lead frame to a semiconductor | | separated from each other by |
| 600 | chip | | insulator means and forming |
| 673 | .With bumps on ends of lead | | part of a package of housing |
| | fingers to connect to | | (e.g., plural ceramic layer |
| | semiconductor | | package) |
| 674 | .With means for controlling lead | 701 | .Insulating material |
| | tension | 702 | Of insulating material other |
| 675 | .With heat sink means | | than ceramic |
| 676 | .With structure for mounting | 703 | Composite ceramic, or single |
| | semiconductor chip to lead | | ceramic with metal |
| | frame (e.g., configuration of | 704 | Cap or lid |
| | die bonding flag, absence of a | 705 | Of high thermal conductivity |
| | die bonding flag, recess for | | ceramic (e.g., BeO) |
| | LED) | 706 | With heat sink |
| 677 | .Of specified material other than | 707 | Directly attached to |
| | copper (e.g., Kovar (T.M.)) | | semiconductor device |
| 678 | HOUSING OR PACKAGE | 708 | .Entirely of metal except for |
| 679 | .Smart (e.g., credit) card | | feedthrough |
| | package | 709 | With specified insulator to |
| 680 | .(E) With window means | | isolate device from housing |
| 681 | (E) For erasing EPROM | 710 | With specified means (e.g., |
| 682 | .With desiccant, getter, or gas | | lip) to seal base to cap |
| | filling | 711 | With raised portion of base for |
| 683 | .With means to prevent explosion | | mounting semiconductor chip |
| | of package | 712 | .With provision for cooling the |
| 684 | .With semiconductor element | | housing or its contents |
| | forming part (e.g., base, of | 713 | For integrated circuit |
| | housing) | 714 | Liquid coolant |
| 685 | .(E) Multiple housings | 715 | Boiling (evaporative) liquid |
| 686 | (E) Stacked arrangement | 716 | Cryogenic liquid coolant |
| 687 | .Housing or package filled with | 717 | Isolation of cooling means |
| | solid or liquid electrically | | (e.g., heat sink) by an |
| | insulating material | | electrically insulating |
| 688 | .With large area flexible | | element (e.g., spacer) |
| | electrodes in press contact | 718 | Heat dissipating element held |
| | with opposite sides of active | | in place by clamping or spring |
| | semiconductor chip and | | means |
| | surrounded by an insulating | 719 | Pressed against semiconductor |
| | <pre>element (e.g., ring)</pre> | - | element |
| | | | |

| 720 | Host dissination alamant has | 748 | Dlumal langua of amonified |
|-------|--------------------------------------------------------|-------------|-------------------------------------------------------|
| 720 | Heat dissipating element has | 740 | Plural layers of specified |
| | high thermal conductivity insert (e.g., copper slug in | 749 | contact or lead material(E) At least portion of which |
| | aluminum heat sink) | 749 | |
| 721 | With gas coolant | | is transparent to ultraviolet, |
| 721 | With fins | 750 | visible or infrared light(E) Layered |
| 723 | | | . , |
| | .(E) For plural devices | 751 | (E) At least one layer forms a |
| 724 | (E) With discrete components | 550 | diffusion barrier |
| 725 | (E) With electrical isolation | 752 | Planarized to top of |
| | means | | insulating layer |
| 726 | \dots (E) Devices held in place by | 753 | With adhesion promoting means |
| | clamping | | (e.g., layer of material) to |
| 727 | .Device held in place by clamping | | promote adhesion of contact to |
| 728 | .For high frequency (e.g., | | an insulating layer |
| | microwave) device | 754 | \dots (E) At least one layer of |
| 729 | .Portion of housing of specific | | silicide or polycrystalline |
| | materials | | silicon |
| 730 | .Outside periphery of package | 755 | Polysilicon laminated with |
| | having specified shape or | | silicide |
| | configuration | 756 | Multiple polysilicon layers |
| 731 | .With housing mount | 757 | Silicide of refractory or |
| 732 | Flanged mount | | platinum group metal |
| 733 | Stud mount | 758 | Multiple metal levels on |
| 734 | (E) COMBINED WITH ELECTRICAL | | semiconductor, separated by |
| , 5 1 | CONTACT OR LEAD | | insulating layer (e.g., |
| 735 | Beam leads (i.e., leads that | | multiple level metallization |
| 733 | | | for integrated circuit) |
| | extend beyond the ends or | 759 | (E) Including organic |
| 726 | sides of a chip component) | , 3,5 | insulating material between |
| 736 | Layered | | metal levels |
| 737 | .Bump leads | 760 | Separating insulating layer |
| 738 | Ball shaped | 700 | is laminate or composite of |
| 739 | .With textured surface | | plural insulating materials |
| 740 | .With means to prevent contact | | (e.g., silicon oxide on |
| | from penetrating shallow pn | | silicon nitride, silicon |
| | junction (e.g., prevention of | | oxynitride) |
| | aluminum "spiking") | 761 | At least one layer containing |
| 741 | .(E) Of specified material other | 701 | vanadium, hafnium, niobium, |
| | than unalloyed aluminum | | zirconium, or tantalum |
| 742 | With a semiconductor | 762 | <i>,</i> |
| | conductivity substitution type | 762 | At least one layer containing |
| | dopant (e.g., germanium in the | 5 60 | silver or copper |
| | case of a gallium arsenide | 763 | At least one layer of |
| | semiconductor) in a contact | | molybdenum, titanium, or |
| | metal | | tungsten |
| 743 | For compound semiconductor | 764 | Alloy containing molybdenum, |
| | contact material | | titanium, or tungsten |
| 744 | (E) For compound semiconductor | 765 | At least one layer of an alloy |
| | material | | containing aluminum |
| 745 | (E) Contact for III-V material | 766 | At least one layer containing |
| 746 | Composite material (e.g., | | chromium or nickel |
| | fibers or strands embedded in | 767 | Resistive to electromigration |
| | solid matrix) | | or diffusion of the contact or |
| 747 | With thermal expansion matching | | lead material |
| | of contact or lead material to | | |
| | semiconductor active device | | |
| | | | |

| 768 | Refractory or platinum group | tain both U.S. and foreign documents. New |
|----------|--------------------------------------------------------------|-------------------------------------------------------|
| | metal or alloy or silicide | U.S. documents are classified here by the |
| | thereof | USPTO, and European foreign by the EPO. E- |
| 769 | Platinum group metal or | subclasses may contain subject matter out- |
| | silicide thereof | side the scope of this class. Consult |
| 770 | Molybdenum, tungsten, or | their definitions, or the documents them- |
| | titanium or their silicides | selves to clarify or interpret titles. |
| 771 | Alloy containing aluminum | |
| 772 | Solder composition | T00 001 |
| 773 | .(E) Of specified configuration | E29.001 DETAILS OF SEMICONDUCTOR BODIES |
| 774 | Via (interconnection hole) | OR ELECTRODES OF SEMICONDUCTOR |
| | shape | DEVICES ADAPTED FOR |
| 775 | Varying width or thickness of | RECTIFYING, AMPLIFYING, OSCILLATING OR SWITCHING, OR |
| | conductor | CAPACITORS OR RESISTORS WITH |
| 776 | Cross-over arrangement, | AT LEAST ONE POTENTIAL-JUMP |
| | component or structure | BARRIER OR SURFACE BARRIER |
| 777 | .Chip mounted on chip | (E.G., PN JUNCTION DEPLETION |
| 778 | .Flip chip | LAYER OR CARRIER CONCENTRATION |
| 779 | .Solder wettable contact, lead or | LAYER) (EPO) |
| E00 | bond | E29.002 .Electrical characteristics due |
| 780 | .Ball or nail head type contact, | to properties of entire |
| E01 | lead or bond | semiconductor body rather than |
| 781 | Layered contact, lead or bond | just surface region (EPO) |
| 782 | .Die bond | E29.003Characterized by their |
| 783 | With adhesive means | crystalline structure (e.g., |
| 784 | .Wire contact, lead or bond | polycrystalline, cubic) |
| 785 | .By pressure alone | particular orientation of |
| 786 | .Configuration or pattern of bonds | crystalline planes (EPO) |
| 787 | ENCAPSULATED | E29.004With specified crystalline |
| 788 | | planes or axis (EPO) |
| 789 | .With specified encapsulant | E29.005Characterized by specified |
| 790 | With specified filler material | shape or size of PN junction or by specified impurity |
| 791 | Plural encapsulating layers | concentration gradient within |
| 791 | <pre>Including polysiloxane (e.g., silicone resin)</pre> | device (EPO) |
| 792 | (E) Including polyimide | E29.006Characterized by particular |
| 793 | (E) Including epoxide | design considerations to |
| 794 | Including glass | control electrical field |
| 795 | .With specified filler material | effect within device (EPO) |
| 796 | .With heat sink embedded in | E29.007For controlling surface |
| , , , | encapsulant | leakage or electric field |
| 797 | ALIGNMENT MARKS | concentration (EPO) |
| 798 | MISCELLANEOUS | E29.008For controlling breakdown |
| , , , | 11200111111000 | voltage of reverse biased |
| | | devices (EPO) |
| | | E29.009With field relief electrode |
| E-SIID | LASSES | (field plate) (EPO) |
| <u> </u> | <u> </u> | E29.01With at least two field |
| | | relief electrodes used in |
| | | |

combination and not

(EPO)

electrically interconnected

The following subclasses beginning with the letter E are E-subclasses. Each E-subclass corresponds to a classification in the European Classification system (ECLA). The ECLA classification is parenthesized at the end of the title. E-subclasses con-

| E29.011With one or more field relief electrode comprising resistance material (e.g., | E29.025Characterized by particular shape of junction between semiconductor regions (EPO) |
|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <pre>semi insulating material, lightly doped poly-sillcon)</pre> | E29.026Surface layout of the device (EPO) |
| <pre>poly-silicon) (EPO) E29.012By doping profile or shape</pre> | E29.027Surface layout of MOS gated device (e.g., DMOSFET or IGBT) |
| or arrangement of PN junction, or with supplementary regions | (EPO) E29.028With nonplanar gate |
| <pre>(e.g., guard ring, LDD, drift region) (EPO)</pre> | structure (EPO) E29.029With semiconductor regions |
| E29.013With supplementary region doped oppositely to or in rectifying contact with semiconductor containing or contacting region (e.g., guard rings with PN or Schottky junction) (EPO) | connected to electrode carrying current to be rectified, amplified, or switched and such electrode being part of semiconductor device which comprises three or more electrodes (EPO) |
| E29.014With breakdown supporting region for localizing | E29.03Emitter regions of bipolar transistors (EPO) |
| breakdown or limiting its voltage I82 (EPO) | E29.031Of lateral transistors (EPO) E29.032Noninterconnected |
| E29.015With insulating layer | multiemitter structures (EPO) |
| characterized by dielectric or electrostatic property (e.g., | E29.033Of heterojunction bipolar transistors (EPO) |
| <pre>including fixed charge or semi-insulating surface layer) (EPO)</pre> | E29.034Collector regions of bipolar transistors (EPO) |
| E29.016For preventing surface | E29.035Pedestal collectors (EPO) |
| <pre>leakage due to surface inversion layer (e.g., channel stop) (EPO)</pre> | E29.036Anode or cathode regions of thyristors or gated bipolar-mode devices (EPO) |
| E29.017With field relief electrodes acting on insulator | E29.037Anode regions of thyristors or gated bipolar-mode devices (EPO) |
| potential or insulator charges (EPO) | E29.038Cathode regions of thyristors (EPO) |
| E29.018Comprising internal isolation within devices or components (EPO) | E29.039Source or drain regions of field-effect devices (EPO) |
| E29.019Isolation by PN junctions (EPO) | E29.04Of field-effect transistors with insulated gate (EPO) |
| E29.02Isolation by dielectric regions (EPO) | E29.041Of field-effect transistors with Schottky gate (EPO) |
| E29.021For source or drain region of field effect device (EPO) | E29.042Tunneling barrier (EPO) E29.043With semiconductor regions |
| E29.022Characterized by shape of semiconductor body (EPO) | connected to electrode not carrying current to be |
| E29.023Adapted for altering junction breakdown voltage by shape of semiconductor body (EPO) | rectified, amplified, or switched and such electrode being part of semiconductor device which comprises three |
| E29.024Characterized by shape, relative sizes or dispositions | or more electrodes (EPO) |
| of semiconductor regions or | E29.044Base region of bipolar transistors (EPO) |
| junctions between regions (EPO) | E29.045Of lateral transistors (EPO) |

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| E29.046Base regions of thyristors (EPO) | E29.072Structures with periodic or quasi periodic potential |
|-----------------------------------------------|--------------------------------------------------------------------|
| E29.047Anode base regions of thyristors (EPO) | <pre>variation (e.g., multiple quantum wells, superlattices)</pre> |
| E29.048Cathode base regions of | (EPO) E29.073Doping structures (e.g., |
| thyristors (EPO) | |
| E29.049Channel region of field- | doping superlattices, nipi- |
| effect devices (EPO) | superlattices) (delta doping, |
| E29.05Of field-effect transistors | in general) (EPO) |
| (EPO) | E29.074Structures without potential |
| E29.051With insulated gate (EPO) | periodicity in direction |
| E29.052Nonplanar channel (EPO) | perpendicular to major surface |
| E29.053With nonuniform doping | of substrate (e.g., lateral |
| structure in channel region | superlattice) (EPO) |
| surface (EPO) | E29.075Compositional structures |
| E29.054Doping structure being | (EPO) |
| parallel to channel length | E29.076With layered structures with |
| (EPO) | quantum effects in vertical |
| E29.055With vertical doping | direction (EPO) |
| variation (EPO) | E29.077Compromising at least one |
| E29.056With variation of | long-range structually |
| composition of channel (EPO) | disordered material (e.g., |
| E29.057With PN junction gate (EPO) | one-dimensional vertical |
| E29.058Of charge coupled devices | amorphous superlattices) (EPO) |
| (EPO) | E29.078Comprising only |
| E29.059Gate region of field-effect | semiconductor materials (EPO) |
| devices with PN junction gate | E29.079 Two or more elements from two |
| (EPO) | or more groups of Periodic |
| E29.06Substrate region of field- | Table of elements (e.g., |
| effect devices (EPO) | alloys) (EPO) |
| E29.061Of field-effect transistors | E29.08Amorphous materials (EPO) |
| (EPO) | E29.081In different semiconductor |
| E29.062With insulated gate (EPO) | regions (e.g., |
| E29.063With inactive | heterojunctions) (EPO) |
| supplementary region (e.g., | E29.082Only element from fourth group |
| for preventing punch-through, | of Periodic System in |
| improving capacity effect or | uncombined form (EPO) |
| leakage current) (EPO) | E29.083Amorphous materials (EPO) |
| E29.064Characterized by contact | E29.084Including two or more of |
| structure of substrate region | elements from fourth group of |
| (EPO) | Periodic System (EPO) |
| E29.065Of charge coupled devices | E29.085In different semiconductor |
| (EPO) | regions (e.g., |
| E29.066Body region structure IGFET's | heterojunctions) (EPO) |
| with channel containing layer | E29.086Further characterized by |
| (DMOSFET or IGBT) (EPO) | doping material (EPO) |
| E29.067With nonplanar gate | E29.087 Selenium or tellurium only |
| structure (EPO) | (EPO) |
| E29.068Characterized by materials of | E29.088 Amorphous materials (EPO) |
| semiconductor body (EPO) | E29.089Only Group III-V compounds |
| E29.069Single quantum well structures | (EPO) |
| (EPO) | E29.09Including two or more |
| E29.07Quantum wire structures (EPO) | compounds (e.g. alloys) (EPO) |
| | |
| E29.071Quantum box or quantum dot | |

structures (EPO)

| E29.091 | <pre>In different semiconductor regions (e.g., heterojunctions)</pre> | E29.112 | Characterized by their shape, relative sizes, or dispositions (EPO) |
|---------|---------------------------------------------------------------------------------------------------------------|---------|---------------------------------------------------------------------------------------------------------|
| E29.092 | heterojunctions (EPO)Amorphous materials (EPO) | E29.113 | Carrying current to be rectified, amplified, or |
| E29.093 | Further characterized by doping material (EPO) | E29.114 | <pre>switched (EPO)Emitter or collector</pre> |
| E29.094 | Only GROUP II-VI compounds (EPO) | | electrodes for bipolar transistors (EPO) |
| | Amorphous materials (EPO)Including two or more | E29.115 | <pre>Cathode or anode electrodes for thyristors (EPO)</pre> |
| E29.097 | compounds (e.g. alloys) (EPO)In different semiconductor | E29.116 | Source or drain electrodes for field effect devices (EPO) |
| | <pre>regions (e.g., heterojunctions) (EPO)</pre> | E29.117 | For thin film transistors with insulated gate (EPO) |
| E29.098 | Further characterized by doping material (EPO) | E29.118 | For vertical current flow (EPO) |
| E29.099 | <pre>Cd X compounds being one element of the sixth group of Periodic System (EPO)</pre> | E29.119 | For lateral devices where connection to source or drain region is done through at |
| E29.1 | <pre>Semiconductor materials other than Group IV, selenium, tellurium, or Group III-V compounds (EPO)</pre> | | <pre>least one part of semiconductor substrate thickness (e.g., with connecting sink or with via-</pre> |
| E29.101 | Amorphous materials (EPO) | | hole) (EPO) |
| | Group I-VI or I-VII compounds (e.g., Cu20, CuI) (EPO) | E29.12 | Layout configuration for lateral device source or drain |
| | Pb compounds (e.g., PbO) (EPO) | | region (e.g., cellular, interdigitated or ring structure or being curved or |
| | Si compounds (e.g., SiC) (EPO) | E20 121 | angular) (EPO)Source or drain electrode in |
| E29.105 | Characterized by combinations of two or more features of | | groove (EPO) |
| | <pre>crystalline structure, shape, materials, physical imperfections, and concentration/distribution of</pre> | E29.122 | Characterized by relative position of source or drain electrode and gate electrode (EPO) |
| | <pre>impurities in bulk material (EPO)</pre> | E29.123 | Not carrying current to be rectified, amplified, or |
| E29.106 | Characterized by physical imperfections; having polished or roughened surface (EPO) | E29.124 | <pre>switched (EPO)Base electrodes for bipolar transistors (EPO)</pre> |
| E29.107 | Imperfections within semiconductor body (EPO) | E29.125 | Gate electrodes for thyristors (EPO) |
| E29.108 | Imperfections on surface of semiconductor body (EPO) | E29.126 | Gate stack for field effect devices (EPO) |
| E29.109 | Characterized by concentration or distribution of impurities | | For field-effect transistors (EPO) |
| | in bulk material (EPO) | | With insulated gate (EPO) |
| E29.11 | Planar doping (e.g., atomic- plane doping, delta-doping) (EPO) | E29.129 | Gate electrodes for transistors with floating gate (EPO) |
| E29.111 | .Electrodes (EPO) | E29.13 | Gate electrodes for nonplanar MOSFET (EPO) |

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| TOO 121 | T00 150 T1 + 1 + 1 + 1 |
|----------------------------------------------------------------|-----------------------------------------------------------------------|
| E29.131Having drain and source | E29.158Elemental metal gate |
| regions at different vertical | conductor material (e.g. W, |
| level having channel composed | Mo) (EPO) |
| only of vertical sidewall | E29.159Diverse conductors (EPO) |
| connecting drain and source layers (EPO) | E29.16Gate conductor material being compound or alloy material |
| E29.132Characterized by insulating layer (EPO) | (e.g., organic material, TiN, MoSi2) (EPO) |
| E29.133Nonuniform insulating | E29.161Silicide (EPO) |
| layer thickness (EPO) | E29.162Insulating materials for |
| E29.134Characterized by | IGFET (EPO) |
| <pre>configuration of gate electrode layer (EPO)</pre> | E29.164With at least one ferroelectric layer (EPO) |
| E29.135Characterized by length | E29.165Multiple layers (EPO) |
| or sectional shape (EPO) | E29.166 .Types of semiconductor |
| E29.136Characterized by surface | semiconductor device (EPO) |
| lay-out (EPO) | |
| E29.137Characterized by | E29.167Controllable by plural effects that include variations in |
| _ | |
| <pre>configuration of gate stack of thin film FETs (EPO)</pre> | magnetic field, mechanical |
| | force, or electric current/ |
| E29.138For charge coupled devices | potential applied to device or |
| (EPO) | one or more electrodes of |
| E29.139 Of specified material (EPO) | device (EPO) |
| E29.14For gate of heterojunction field effect devices (EPO) | E29.168Quantum effect device (EPO) E29.169Controllable by only signal |
| E29.141Resistive materials for field | applied to control electrode |
| effect devices (EPO) | (e.g., base of bipolar |
| E29.142 Superconductor materials (EPO) | transistor, gate of field |
| E29.143Ohmic electrodes (EPO) | effect transistor) (EPO) |
| E29.144On Group III-V material (EPO) | E29.17 Memory effect devices (EPO) |
| E29.145On thin film Group III-V | E29.171Bipolar device (EPO) |
| material (EPO) | E29.172Double base diode (EPO) |
| E29.146On silicon (EPO) | E29.173Transistor-type device (i.e., |
| E29.147For thin film silicon (EPO) | able to continuously respond |
| E29.148Schottky barrier electrodes | to applied control signal) |
| (EPO) | (EPO) |
| E29.149On Group III-V material (EPO) | E29.174Bipolar junction transistor |
| E29.15Electrodes for IGFET (EPO) | (EPO) |
| E29.151For TFT (EPO) | E29.175Structurally associated |
| | with other devices (EPO) |
| E29.152With lateral structure (e.g., | E29.176Device being resistive |
| Poly-silicon gate with lateral doping variation or with | element (e.g., ballasting |
| lateral composition variation | resistor) (EPO) |
| or characterized by sidewalls | E29.177Point contact transistors |
| being composed of conductive, | (EPO) E20 178 Cabottky transistors (EDO) |
| resistivity) (EPO) | E29.178Schottky transistors (EPO) E29.179Tunnel transistors (EPO) |
| E29.154Silicon gate conductor | • |
| material (EPO) | E29.18Avalanche transistors (EPO) |
| E29.155Multiple silicon layers | E29.181 Transistors with hook |
| (EPO) | collectorie (i.e., collector |
| E29.156Including silicide layer | having two layers of opposite |
| contacting silicon layer (EPO) | conductivity type (e.g., SCR)) |
| E29.157Including barrier layer | (EPO) |
| between silicon and non-Si | E29.182Bipolar thin film |
| electrode (EPO) | transistors (EPO) |
| | |

| ₽20 102 | Vertical transistor (EPO) | E29.2With nonplanar surface |
|---------|---------------------------------------------------------------|---------------------------------------------|
| | Having emitter-base and | (e.g., with a nonplanar gate |
| E29.104 | base-collector junctions in | or with trench or recess or |
| | same plane (EPO) | pillar in surface of emitter, |
| E20 10E | Having emitter-base | base, or collector region for |
| E27.105 | junction and base-collector | improving current density or |
| | junction and base-corrector junction on different surfaces | short circuiting emitter and |
| | (e.g., mesa planar transistor) | base regions) (EPO) |
| | (EPO) | E29.201And gate structure lying |
| E29.186 | Inverse vertical | on slanted or vertical surface |
| | transistor (EPO) | or formed in groove (e.g., |
| E29.187 | Lateral transistor (EPO) | trench gate IGBT) (EPO) |
| | Hetero-junction transistor | E29.202Thin film device (EPO) |
| | (EPO) | E29.211Thyristor-type device (e.g., |
| E29.189 | Vertical transistors (EPO) | having four-zone regenerative |
| | | action) (EPO) |
| | base (e.g., modulation-doped | E29.212Gate-turn-off device (EPO) |
| | base, inversion layer base, | E29.213With turn off by field |
| | delta-doped base) (EPO) | effect (EPO) |
| E29.191 | | E29.214Produced by insulated gate |
| | comprising one or more non- | structure (EPO) |
| | monocrystalline elements of | E29.215Bidirectional device (e.g., |
| | Group IV (e.g., amorphous | triac) (EPO) |
| | silicon) alloys comprising | E29.216With turn on by field effect |
| | Group IV elements (EPO) | (EPO) |
| E29.192 | Resonant tunneling | E29.217Combined structurally with |
| | transistors (EPO) | at least one other device |
| E29.193 | Comprising lattice | (EPO) |
| | mismatched active layers | E29.218Combined with capacitor or |
| | (e.g., SiGe strained layer | resistor (EPO) |
| E20 104 | transistors) (EPO) | E29.219Combined with diode (EPO) |
| E29.194 | Controlled by field effect | E29.22Antiparallel diode (EPO) |
| | <pre>(e.g., bipolar static induction transistor (BSIT))</pre> | E29.221Combined with field effect |
| | (EPO) | transistor (EPO) |
| E29 195 | Gated diode structure (EPO) | E29.222Having built-in localized |
| | With PN junction gate | <pre>breakdown/breakover region (EPO)</pre> |
| | (e.g., field controlled | E29.223Having amplifying gate |
| | thyristor (FCTh), static | structure (e.g., Darlington |
| | induction thyristor (SITh)) | configuration) (EPO) |
| | (EPO) | E29.224Asymmetrical thyristor (EPO) |
| E29.197 | Insulated gate bipolar mode | E29.225Lateral thyristor (EPO) |
| | transistor (e.g., IGBT, IGT, | E29.226Unipolar device (EPO) |
| | COMFET) (EPO) | E29.227Charge transfer device (EPO) |
| E29.198 | Transistor with vertical | E29.228Charge-coupled device (EPO) |
| | current flow (EPO) | E29.229With field effect produced |
| E29.199 | With both emitter and | by insulated gate (EPO) |
| | collector contacts in same | E29.23Input structure (EPO) |
| | substrate side (EPO) | E29.231Output structure (EPO) |
| | | E29.232Structure for improving |
| | | output signal (EPO) |
| | | E29.233Buried channel CCD (EPO) |
| | | E29.234Two-phase CCD (EPO) |
| | | E29.235Three-phase CCD (EPO) |
| | | E29.236Four-phase CCD (EPO) |
| | | |

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| E29.238 E29.239 | Surface channel CCD (EPO)Two-phase CCD (EPO)Three-phase CCD (EPO)Four-phase CCD (EPO) | E29.257 | Having a vertical bulk current component or current vertically following a trench gate, (e.g., vertical power |
|--------------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------|---------------------------------------------------------------------------------------------------------------------------------------------------------------|
| E29.241 | Hot electron transistor (HET) or metal base transistor (MBT) (EPO)Field effect transistor (EPO) | E29.258 | DMOS transistor) (EPO)With both source and drain contacts in same substrate side (EPO) |
| | Using static field induced region (e.g., SIT, PBT) (EPO) | | With nonplanar surface (EPO) |
| | Velocity modulations transistor (i.e., VMT) (EPO) | E29.26 | |
| E29.245 | <pre>With one-dimensional charge carrier gas channel (e.g., quantum wire FET) (EPO)</pre> | | surface of groove (e.g., trench gate DMOSFET) (EPO) |
| E29.246 | <pre>With two-dimensional charge carrier gas channel (e.g,. HEMT; with two-dimensional charge-carrier layer formed at heterojunction interface) (EPO)</pre> | E29.262 | With at least part of active region on insulating substrate (e.g., lateral DMOS in oxide isolated well) (EPO)Vertical transistor (EPO)Comprising gate to body |
| E29.247 | With inverted single heterostructure (i.e., with active layer formed on top of wide bandgap layer (e.g., | | connection (i.e., bulk dynamic threshold voltage MOSFET) (EPO)With multiple gate |
| | IHEMT)) (EPO) | | structure (EPO) |
| E29.248 | <pre>With confinement of carriers by at least two heterojunctions (e.g., DHHEMT, quantum well HEMT, DHMODFET)</pre> | E29.265 | Structure comprising MOS gate and at least one non-MOS gate (e.g., JFET or MESFET gate) (EPO) |
| | (EPO) | E29.266 | With lightly doped drain or |
| E29.249 | Using Group III-V | ₽20 267 | source extension (EPO)With nonplanar structure |
| E29.25 | semiconductor material (EPO)With more than one donor layer (EPO) | 127.207 | (e.g., gate or source or drain being nonplanar) (EPO) |
| | With delta or planar doped donor layer (EPO) | E29.268 | The source region and drain region having a non- |
| E29.252 | With direct single heterostructure (i.e., with | | <pre>symmetrical structure about the gate electrode (EPO)</pre> |
| | wide bandgap layer formed on top of active layer (e.g., | E29.269 | With overlap between lightly doped extension and gate electrode (EPO) |
| | <pre>direct single heterostruture MIS-like HEMT)) (EPO)</pre> | E29.27 | With buried channel (EPO) |
| E29.253 | With wide bandgap charge- carrier supplying layer (e.g., | | With Schottky drain or source contact (EPO) |
| | <pre>direct single heterostructure MODFET) (EPO)</pre> | | Gate comprising ferroelectric layer (EPO) |
| E29.254 | With delta-doped channel (EPO) | | Thin film transistor (EPO)Vertical transistor (EPO) |
| E29.255 | <pre>With field effect produced by insulated gate (EPO)</pre> | | With multiple gates (EPO)With supplementary region |
| E29.256 | With channel containing layer contacting drain drift region (e.g., DMOS transistor) (EPO) | | or layer in thin film or in insulated bulk substrate supporting it for controlling or increasing voltage resistance of device (EPO) |

| | Characterized by drain or source properties (EPO) | | With floating gate (EPO)Programmable by two single |
|---------|-----------------------------------------------------------------------------------------------|---------|--------------------------------------------------------------------------------------------|
| E29.278 | With LDD structure or extension or offset region or characterized by doping | E29.302 | <pre>electrons (EPO)Hi-lo programming levels only (EPO)</pre> |
| E29.279 | <pre>profile (EPO)Asymmetrical source and drain regions (EPO)</pre> | E29.303 | Charging by injection of carriers through conductive insulator (e.g., Poole-Frankel |
| E29.28 | For preventing leakage current (EPO) | E29.304 | <pre>conduction) (EPO)Charging by tunneling of</pre> |
| E29.281 | For preventing kink or snapback effect (e.g., | | carriers, (e.g. Fowler- Nordheim tunneling) (EPO) |
| | discharging minority carriers of channel region for | | |
| -00 000 | preventing bipolar effect) (EPO) | | |
| | With light shield (EPO)With supplementary region or layer for improving | E29.307 | |
| E29.284 | flatness of device (EPO)With drain or source connected to bulk conducting | E29.308 | Programmable with more than two possible different levels (EPO) |
| E20 20E | substrate (EPO) | E29.309 | With charge trapping gate |
| | Silicon transistor (EPO)Monocrystalline only | | <pre>insulator (e.g., MNOS-memory transistors) (EPO)</pre> |
| | (EPO)SOS transistor (EPO)Nonmonocrystalline (EPO) | E29.31 | <pre>With field effect produced by PN or other rectifying junction gate (i.e.,</pre> |
| | Amorphous silicon transistor (EPO) | E29.311 | potential-jump barrier) (EPO)With Schottky drain or |
| | With top gate (EPO) | | source contact (EPO) |
| | transistor structure (EPO) | E29.312 | <pre>With PN junction gate (e.g., PN homojunction gate)</pre> |
| E29.292 | Polycrystalline or microcrystalline silicon transistor (EPO) | | (EPO)Vertical transistors (EPO)Thin film JFET (EPO) |
| | With top gate (EPO) | E29.315 | With heterojunction gate |
| | With inverted transistor structure (EPO) | | (e.g., transistors with semiconductor layer acting as |
| E29.295 | <pre>Characterized by insulating substrate or support (EPO)</pre> | E29.316 | <pre>gate insulating layer) (EPO)Programmable transistor (e.g., with charge-trapping</pre> |
| E29.296 | Comprising Group III-V or II-VI compound, or of Se, Te, | E29.317 | quantum well) (EPO)With Schottky gate (EPO) |
| -00 005 | or oxide semiconductor (EPO) | E29.318 | Vertical transistors (EPO) |
| E29.297 | <pre>Comprising Group IV non-Si semiconductor materials or alloys (e.g., Ge, SiN alloy,</pre> | E29.32 | With multiple gate (EPO)Thin film MESFET (EPO)With recessed gate (EPO) |
| E29.298 | SiC alloy) (EPO)With multilayer structure | | Single electron transistors: Coulomb blockade device (EPO) |
| | or superlattice structure (EPO) | E29.323 | Controllable by variation of magnetic field applied to |
| E29.299 | <pre>Characterized by property or structure of channel or contact thereto (EPO)</pre> | | device (EPO) |

| | Controllable by variation of applied mechanical force (e.g., of pressure) (EPO)Controllable only by variation of electric current supplied or only electric potential | E27.001 | DEVICE CONSISTING OF A PLURALITY OF SEMICONDUCTOR OR OTHER SOLID STATE COMPONENTS FORMED IN OR ON A COMMON SUBSTRATE, E.G., INTEGRATED CIRCUIT DEVICE (EPO) |
|---------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------|-------------------------------------------------------------------------------------------------------------------------------------------------------------|
| | applied to electrode carrying | E27.002 | .Including bulk negative |
| | current to be rectified, amplified, oscillated, or | | resistance effect component (EPO) |
| E29.326 | <pre>switched (EPO)Resistor with PN junction</pre> | E27.003 | Including Gunn-effect device (EPO) |
| | (EPO) | E27.004 | .Including solid state component |
| | Diode (EPO) | | for rectifying, amplifying, or |
| | Planar PN junction diode (EPO) | | switching without a potential barrier or surface barrier |
| | Mesa PN junction diode (EPO) | | (EPO) |
| E29.33 | <pre>Hi-lo semiconductor device (e.g., memory device) (EPO)</pre> | E27.005 | .Including component using galvano-magnetic effects, |
| | Charge trapping diode (EPO) | | e.g., Hall effect (EPO) |
| E29.332 | Punchthrough diode (i.e., | E27.006 | .Including piezo-electric, |
| | with bulk potential barrier | | electro-resistive, or magneto- resistive component (EPO) |
| | <pre>(e.g., camel diode, planar doped barrier diode, graded</pre> | E27 007 | .Including superconducting |
| | bandgap diode)) (EPO) | | component (EPO) |
| E29.333 | Point contact diode (EPO) | E27.008 | .Including thermo-electric or |
| | Transit-time diode (e.g., IMPATT, TRAPATT diode) (EPO) | | thermo-magnetic component with or without a junction of |
| E29.335 | Avalanche diode (e.g., Zener | | dissimilar material or thermo- |
| | diode) (EPO) | | magnetic component (EPO) |
| | PIN diode (EPO) | E27.009 | .Including semiconductor |
| E29.337 | Thyristor diode (i.e., having | | component with at least one potential barrier or surface |
| | only two terminals and no | | barrier adapted for |
| | control (gate) electrode | | rectifying, oscillating, |
| | <pre>(e.g., Shockley diode, break- over diode)) (EPO)</pre> | | amplifying, or switching, or |
| E29.338 | Schottky diode (EPO) | | including integrated passive |
| | Tunneling diode (EPO) | | circuit elements (EPO) |
| E29.34 | Resonant tunneling diode (i.e., RTD, RTBD) (EPO) | E27.01 | With semiconductor substrate only (EPO) |
| E29.341 | Esaki diode (EPO) | E27.011 | Including a plurality of |
| | Capacitor with potential-jump barrier or surface barrier | | components in a non-repetitive configuration (EPO) |
| | (EPO) | E27.012 | Made of compound |
| E29.343 | Conductor-insulator-conductor capacitor on semiconductor | | semiconductor material, e.g. III-V material (EPO) |
| | substrate (EPO) | E27.013 | Integrated circuit having a |
| E29.344 | Variable capacitance diode (e.g., varactors) (EPO) | | two-dimensional layout of components without a common |
| E29.345 | Metal-insulator-semiconductor | E27 N14 | <pre>active region (EPO)Including a field-effect</pre> |
| E20 246 | (e.g., MOS capacitor) (EPO) | 227.V14 | type component (EPO) |
| | Trench capacitor (EPO)Controllable by thermal signal | E27.015 | In combination with bipolar transistor (EPO) |
| | (e.g., IR) (EPO) | E27 016 | In combination with diode, |
| | | | resistor, or capacitor (EPO) |

| E27.017In combine transistor a | | E27.036 | With component other than field effect type (EPO) |
|--------------------------------|---------------------------------------|---------|---------------------------------------------------|
| resistor, or | capacitor (EPO) | E27.037 | Bipolar transistor in |
| E27.018With compo | onent other than | | combination with diode, |
| field-effect | type (EPO) | | capacitor, or resistor (EPO) |
| E27.019Bipolar t | | E27.038 | Vertical bipolar |
| combination | | | transistor in combination with |
| | or resistor (EPO) | | diode, capacitor, or resistor |
| E27.02Vertical | | | (EPO) |
| | n combination with | E27.039 | Vertical bipolar |
| | citor, or resistor | | transistor in combination with |
| (EPO) | | | diode only (EPO) |
| E27.021Vertica | al bipolar | E27.04 | With Schottky diode only |
| transistor i | n combination with | | (EPO) |
| resistor or | capacitor only | E27.041 | Vertical bipolar |
| (EPO) | | | transistor in combination with |
| E27.022Vertica | al bipolar | | resistor only (EPO) |
| transistor i | n combination with | E27.042 | Vertical bipolar |
| diode only (| EPO) | | transistor in combination with |
| E27.023Lateral | bipolar transistor | | capacitor only (EPO) |
| in combinati | on with diode, | E27.043 | Lateral bipolar transistor |
| capacitor, c | or resistor (EPO) | | in combination with diode, |
| E27.024Including | g combination of | | capacitor, or resistor (EPO) |
| diode, capac | citor, or resistor | E27.044 | Including combination of |
| (EPO) | | | diode, capacitor, or resistor |
| E27.025Includir | ng combination of | | (EPO) |
| capacitor or | resistor only | E27.045 | Combination of capacitor |
| (EPO) | | | and resistor (EPO) |
| E27.026Integrated | circuit having a | E27.046 | Including only semiconductor |
| | sional layout (EPO) | | components of a single kind, |
| E27.027Including | | | e.g., all bipolar transistors, |
| on opposite | | | all diodes, or all CMOS (EPO) |
| | or substrate (EPO) | | Resistor only (EPO) |
| E27.028Including of | | | Capacitor only (EPO) |
| | on in common (EPO) | | Varactor diode (EPO) |
| E27.029Including | | E27.05 | Metal-insulated- |
| field effect | = = | | semiconductor (MIS) diode |
| | nation with bipolar | | (EPO) |
| transistor a | • | | Diode only (EPO) |
| | or resistor (EPO) | | Thyristor only (EPO) |
| E27.031In comb | | | Bipolar component only (EPO) |
| | oolar transistor | E27.054 | Combination of lateral and |
| | capacitor, or | | vertical transistors only |
| resistor (EP E27.032In comb | | | (EPO) |
| | | E27.055 | Vertical bipolar transistor |
| | lar transistor and citor, or resistor | | only (EPO) |
| (EPO) | ittor, or resistor | E27.056 | Vertical direct transistor |
| E27.033In combin | nation with diode | | of the same conductivity type |
| | or resistor (EPO) | | having different |
| E27.034In combi | | | characteristics, (e.g., |
| capacitor on | | E07 0E7 | Darlington transistor) (EPO) |
| E27.035In combi | | E2/.U5/ | Vertical complementary |
| resistor onl | | | transistor (EPO) |
| TESISCOI OHI | Y (HEO) | | |

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| E27.058Combination of direct and inverse vertical transistors (e.g., collector acts as emitter) (EPO) | E27.08Unijunction transistor, i.e., three terminal device with only one p-n junction having a negative resistance |
|---------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------|
| E27.059Including field-effect component only (EPO) | region in the I-V characteristic (EPO) |
| E27.06Field-effect transistor with insulated gate (EPO) | E27.081Including field-effect component (EPO) |
| E27.061Combination of depletion and enhancement field-effect transistors (EPO) | E27.082Including bucket brigade type charge coupled device (C.C.D) (EPO) |
| E27.062Complementary MIS (EPO) E27.063Means for preventing a parasitic bipolar action | E27.083Including charge coupled device (C.C.D) or charge injection device (C.I.D) (EPO) |
| between the different transistor regions, e.g., | E27.084Dynamic random access memory, DRAM, structure (EPO) |
| latch-up prevention (EPO) E27.064Combination of complementary transistors having a different structure, | E27.085One-transistor memory cell structure, i.e., each memory cell containing only one transistor (EPO) |
| e.g., stacked CMOS, high- voltage and low-voltage CMOS (EPO) | E27.086Storage electrode stacked over the transistor (EPO) |
| E27.065Including an N-well only | E27.087With bit line higher than capacitor (EPO) |
| in the substrate (EPO) E27.066Including a P-well only in the substrate (EPO) | E27.088With capacitor higher than bit line level (EPO) E27.089Storage electrode having |
| E27.067Including both N- and P-wells in the substrate, e.g., | multiple wings (EPO) E27.09Capacitor extending under |
| twin-tub (EPO) E27.068Schottky barrier gate field- | the transistor (EPO) E27.091Transistor in trench (EPO) |
| effect transistor (EPO) E27.069PN junction gate field- | E27.092Capacitor in trench (EPO) |
| effect transistor (EPO) | E27.093Capacitor extending under or around the transistor (EPO) |
| E27.07Including a plurality of individual components in a repetitive configuration (EPO) | E27.094Having storage electrode extension stacked over the transistor (EPO) |
| E27.071Including resistor or capacitor only (EPO) | E27.095Capacitor and transistor in common trench (EPO) |
| E27.072Including bipolar component (EPO) | E27.096Vertical transistor (EPO) E27.097Peripheral structure (EPO) |
| E27.073Including diode only (EPO) E27.074Including bipolar transistor | E27.098Static random access memory, SRAM, structure (EPO) |
| (EPO) E27.075Bipolar dynamic random | E27.099Load element being a MOSFET transistor (EPO) |
| access memory structure (EPO) E27.076Array of single bipolar | E27.1Load element being a thin film transistor (EPO) |
| transistors only, e.g., read only memory structure (EPO) | E27.101Load element being a resistor (EPO) |
| E27.077Static bipolar memory cell structure (EPO) | E27.102Read-only memory, ROM, |
| E27.078Bipolar electrically programmable memory structure | structure (EPO) E27.103Electrically programmable ROM (EPO) |
| (EPO) E27.079Thyristor (EPO) | E27.104Ferroelectric non-volatile memory structure (EPO) |

| E27.105Masterslice integrated circuit (EPO) | E27.122 .Including active semiconductor component sensitive to |
|------------------------------------------------------------------------|------------------------------------------------------------------------------------------|
| E27.106Using bipolar structure (EPO) | infrared radiation, light, or electromagnetic radiation of a |
| E27.107Using field effect structure (EPO) | shorter wavelength (EPO) E27.123 Energy conversion device (EPO) |
| E27.108CMOS gate array (EPO) | E27.124 In a repetitive configuration, |
| E27.109Using combined field effect/ bipolar structure (EPO) | e.g., planar multi-junction solar cells (EPO) |
| E27.11Input and output buffer/ driver (EPO) | E27.125Including only thin film solar cells deposited on a |
| E27.111 Substrate comprising other than | substrate (EPO) |
| <pre>a semiconductor material, e.g., insulating substrate or</pre> | E27.126Including multiple vertical junction or V-groove junction solar cells formed in a |
| layered substrate including a | semiconductor substrate (EPO) |
| non-semiconductor layer (EPO) | |
| E27.112Including insulator on semiconductor, e.g., SOI | E27.127Device controlled by radiation (EPO) |
| (silicon on insulator) (EPO) | E27.128With at least one potential |
| E27.113Combined with thin-film or | barrier or surface barrier |
| thick-film passive component | (EPO) |
| (EPO) | E27.129In a repetitive configuration (EPO) |
| E27.114 .Including only passive thin-film | E27.13Imager including structural or |
| or thick-film elements on a | functional details of the |
| <pre>common insulating substrate (EPO)</pre> | device (EPO) |
| E27.115Thick-film circuits (EPO) | E27.131Geometry or disposition of |
| E27.116Thin-film circuits (EPO) | pixel-elements, address-lines, |
| E27.116Thin-film circuits (EPO) E27.117 .Including organic material in | or gate-electrodes (EPO) |
| active region (EPO) | E27.132Pixel-elements with |
| E27.118Including semiconductor | integrated switching, control, |
| components sensitive to | storage, or amplification |
| infrared radiation, light, or | elements (EPO) |
| electromagnetic radiation of a | E27.133Photodiode array or MOS |
| shorter wavelength (EPO) | imager (EPO) |
| E27.119Including semiconductor | E27.134Color imager (EPO) |
| components with at least one | E27.135Multicolor imager having a |
| potential barrier, surface | stacked pixel-element |
| barrier, or recombination zone | structure, e.g., npn, npnpn or |
| adapted for light emission | MQW elements |
| (EPO) | E27.136Infrared imager (EPO) |
| E27.12 .Including semiconductor | E27.137Of the hybrid type (e.g., |
| component with at least one | chip-on-chip, bonded |
| potential barrier or surface | substrates) (EPO) |
| barrier adapted for light | E27.138Multispectral infrared |
| emission structurally associated with controlling | imager having a stacked pixel- |
| devices having a variable | element structure, e.g., npn, |
| impedance and not being light | npnpn or MQW structures (EPO) E27.139Anti-blooming (EPO) |
| sensitive (EPO) | E27.14X-ray, gamma-ray, or high |
| E27.121 In a repetitive configuration | energy radiation imager |
| (EPO) | (measuring X-, gamma- or |
| • • | corpuscular radiation (EPO) |
| | SOLPHOULAI INGLACION (DIO) |

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| | <pre>Imager using a photoconductor layer (e.g., single photoconductor layer for all pixels) (EPO)</pre> | E51.004 | Controllable by only signal applied to control electrode (e.g., base of bipolar transistor, gate of field- |
|---------|-------------------------------------------------------------------------------------------------------------------|---------|------------------------------------------------------------------------------------------------------------|
| E27.143 | Color imager (EPO)Infrared imager (EPO) | E51.005 | <pre>effect transistor) (EPO)Field-effect device (e.g.,</pre> |
| E27.144 | <pre>0f the hybrid type (e.g., chip-on-chip, bonded substrates) (EPO)</pre> | | <pre>TFT, FET) (EPO)Insulated gate field-effect transistor (EPO)</pre> |
| | Anti-blooming (EPO)X-ray, gamma-ray, or high | E51.007 | <pre>Comprising organic gate dielectric (EPO)</pre> |
| E27.147 | <pre>energy radiation imagers (EPO)Contact-type imager (e.g.,</pre> | E51.008 | Controllable only by variation of electric current supplied |
| | <pre>contacts document surface) (EPO)</pre> | | or only electric potential applied to electrode carrying |
| E27.148 | <pre>Junction field effect transistor (JFET) imager or static induction transistor (SIT) imager (EPO)</pre> | | <pre>current to be rectified, amplified, oscillated, or switched (e.g., two terminal device) (EPO)</pre> |
| E27.149 | Bipolar transistor imager (EPO) | E51.009 | Comprising Schottky junction (EPO) |
| | <pre>Charge coupled imager (EPO)Structural or functional details (EPO)</pre> | E51.01 | <pre>Comprising organic/organic junction (e.g., heterojunction) (EPO)</pre> |
| E27.152 | Geometry or disposition of pixel-elements, address lines | | <pre>Comprising organic/inorganic heterojunction (EPO)</pre> |
| E27.153 | or gate-electrodes (EPO)Linear CCD imager (EPO) | E51.012 | Radiation-sensitive organic solid-state device (EPO) |
| E27.154 | Area CCD imager (EPO)Frame-interline transfer | E51.013 | Metal-organic semiconductor- metal device (EPO) |
| | (EPO) | E51.014 | Comprising bulk heterojunction (EPO) |
| E27.157 | Interline transfer (EPO)Frame transfer (EPO) | E51.015 | Comprising organic/inorganic heterojunction (EPO) |
| | <pre>CID imager (charge injection device) (EPO)</pre> | E51.016 | Majority carrier device using |
| | CCD or CID color imager (EPO) | | <pre>sensitization of wide band gap semiconductor (e.g., TiO2) (EPO)</pre> |
| E27.16 | Infrared CCD or CID imager (EPO) | E51.017 | Comprising organic |
| E27.161 | Of the hybrid type (e.g., chip-on-chip, bonded substrates) (EPO) | | <pre>semiconductor-organic semiconductor heterojunction (EPO)</pre> |
| E27.162 | Anti-blooming (EPO) | E51.018 | Light-emitting organic solid- |
| E27.163 | Including a photoconductive layer deposited on the CCD | -F1 010 | state device with potential or surface barrier (EPO) |
| | structure (EPO) | | Electrode (EPO) |
| E51.001 | ORGANIC SOLID-STATE DEVICE (EPO) | | Encapsulation (EPO) |
| | .Structural detail of device (EPO) | E51.UZ1 | Arrangements for extracting light from device (e.g., Bragg reflector pair) (EPO) |
| E51.003 | Organic solid-state device | E51.022 | Multicolor organic light- |
| | adapted for rectifying, | | emitting device (OLED) (EPO) |
| | amplifying, oscillating, or switching, or capacitors or resistors with potential or | E51.023 | Molecular electronic device (EPO) |

surface barrier (EPO)

| E51.024 .Selection of material for organic solid-state device (EPO) | E51.045Biomolecule or macromolecule (e.g., proteins, ATP, chlorophyl, beta-carotene, |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| , , | |
| E51.025 For organic solid-state device | lipids, enzymes) (EPO) |
| adapted for rectifying, | E51.046Silicon-containing organic |
| amplifying, oscillating, or | semiconductor (EPO) |
| switching, or capacitors or | E51.047Macromolecular system with low |
| resistors with potential or | molecular weight (e.g., |
| surface barrier (EPO) | cyanine dyes, coumarine dyes, |
| E51.026 For radiation-sensitive or | tetrathiafulvalene) (EPO) |
| light-emitting organic solid- | E51.048Charge transfer complexes |
| state device with potential or | |
| | (EPO) |
| surface barrier (EPO) | E51.049Polycondensed aromatic or |
| E51.027Organic polymer or oligomer | heteroaromatic compound (e.g., |
| (EPO) | pyrene, perylene, pentacene) |
| E51.028Comprising aromatic, | (EPO) |
| heteroaromatic, or arrylic | E51.05Aromatic compound containing |
| chains (e.g., polyaniline, | heteroatom (e.g., |
| polyphenylene, polyphenylene | perylenetetracarboxylic |
| vinylene) (EPO) | dianhydride, perylene |
| E51.029Heteroaromatic compound | tetracarboxylic diimide) (EPO) |
| comprising sulfur or selene | E51.051 Amine compound having at least |
| (e.g., polythiophene) (EPO) | two aryl on amine-nitrogen |
| E51.03Polyethylene dioxythiophene | |
| | atom (e.g., triphenylamine) |
| and derivative (EPO) | (EPO) |
| E51.031Polyphenylenevinylene and | E51.052Langmuir Blodgett film (EPO) |
| derivatives (EPO) | E31.001 SEMICONDUCTOR DEVICE RESPONSIVE |
| Ebl (12) Dolrefluxonono and domirrotirro | |
| E51.032Polyflurorene and derivative | OR SENSITIVE TO |
| (EPO) | ELECTROMAGNETIC RADIATION |
| | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, |
| (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF |
| (EPO) E51.033Comprising aliphatic or | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN- | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) E51.041Coordination compound (e.g., | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being Group VI element) (EPO) |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) E51.04Coordination compound (e.g., porphyrin, phthalocyanine, | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being Group VI element) (EPO) E31.006Comprising only Cu2X/CdX |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) E51.04Coordination compound (e.g., porphyrin, phthalocyanine, metal(II) polypyridine | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being Group VI element) (EPO) E31.006Comprising only Cu2X/CdX heterojunction and X being |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) E51.041Coordination compound (e.g., porphyrin, phthalocyanine, metal(II) polypyridine complexes) (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being Group VI element) (EPO) E31.006Comprising only Cu2X/CdX heterojunction and X being Group VI element (EPO) |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) E51.04Coordination compound (e.g., porphyrin, phthalocyanine, metal(II) polypyridine | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being Group VI element) (EPO) E31.006Comprising only Cu2X/CdX heterojunction and X being Group VI element (EPO) E31.007Comprising only |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) E51.041Coordination compound (e.g., porphyrin, phthalocyanine, metal(II) polypyridine complexes) (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being Group VI element) (EPO) E31.006Comprising only Cu2X/CdX heterojunction and X being Group VI element (EPO) E31.007Comprising only heterojunction including Group |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) E51.041Coordination compound (e.g., porphyrin, phthalocyanine, metal(II) polypyridine complexes) (EPO) E51.042Phthalocyanine (EPO) | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being Group VI element) (EPO) E31.006Comprising only Cu2X/CdX heterojunction and X being Group VI element (EPO) E31.007Comprising only heterojunction including Group I-III-VI compound (e.g., CdS/ |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) E51.041Coordination compound (e.g., porphyrin, phthalocyanine, metal(II) polypyridine complexes) (EPO) E51.042Phthalocyanine (EPO) E51.043Metal complexes comprising | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being Group VI element) (EPO) E31.006Comprising only Cu2X/CdX heterojunction and X being Group VI element (EPO) E31.007Comprising only heterojunction including Group I-III-VI compound (e.g., CdS/ CuInSe2 heterojunction) (EPO) |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) E51.04Carbon compound (e.g., porphyrin, phthalocyanine, metal(II) polypyridine complexes) (EPO) E51.042Phthalocyanine (EPO) E51.043Metal complexes comprising Group IIIB metal (Al, Ga, In, or Ti) (e.g., Tris (8- | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being Group VI element) (EPO) E31.006Comprising only Cu2X/CdX heterojunction and X being Group VI element (EPO) E31.007Comprising only heterojunction including Group I-III-VI compound (e.g., CdS/ CuInSe2 heterojunction) (EPO) E31.008Selenium or tellurium (EPO) |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) E51.04Carbon compound (e.g., porphyrin, phthalocyanine, metal(II) polypyridine complexes) (EPO) E51.042Phthalocyanine (EPO) E51.043Metal complexes comprising Group IIIB metal (Al, Ga, In, | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being Group VI element) (EPO) E31.006Comprising only Cu2X/CdX heterojunction and X being Group VI element (EPO) E31.007Comprising only heterojunction including Group I-III-VI compound (e.g., CdS/ CuInSe2 heterojunction) (EPO) |
| (EPO) E51.033Comprising aliphatic or olefinic chains (e.g., polyN-vinylcarbazol, PVC, PTFE) (EPO) E51.034Polyacetylene or derivatives (EPO) E51.035PolyN-vinylcarbazol and derivative (EPO) E51.036Copolymers (EPO) E51.037Ladder-type polymer (EPO) E51.038Carbon-containing materials (EPO) E51.039Fullerenes (EPO) E51.04Carbon nanotubes (EPO) E51.041Coordination compound (e.g., porphyrin, phthalocyanine, metal(II) polypyridine complexes) (EPO) E51.042Phthalocyanine (EPO) E51.043Metal complexes comprising Group IIIB metal (Al, Ga, In, or Ti) (e.g., Tris (8-hydroxyquinoline) aluminium | ELECTROMAGNETIC RADIATION (E.G., INFRARED RADIATION, ADAPTED FOR CONVERSION OF RADIATION INTO ELECTRICAL ENERGY OR FOR CONTROL OF ELECTRICAL ENERGY BY SUCH RADIATION) (EPO) E31.002 .Characterized by semiconductor body (EPO) E31.003Characterized by semiconductor body material (EPO) E31.004Inorganic materials (EPO) E31.005In different semiconductor regions (e.g., Cu2X/CdX heterojunction and X being Group VI element) (EPO) E31.006Comprising only Cu2X/CdX heterojunction and X being Group VI element (EPO) E31.007Comprising only heterojunction including Group I-III-VI compound (e.g., CdS/ CuInSe2 heterojunction) (EPO) E31.008Selenium or tellurium (EPO) |

(e.g., Ru(II) polypyridine

complexes) (EPO)

E31.01Characterized by doping

material (EPO)

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| E31.011 | Including, apart from doping material or other impurity, | | Characterized by doping material (EPO) |
|---------|-------------------------------------------------------------------------------------------------|---------|----------------------------------------------------------------------------------|
| E31.012 | only Group IV element (EPO)For device having potential | | naracterized by semiconductor ody shape, relative size, or |
| E31.013 | or surface barrier (EPO)Comprising porous silicon as | | isposition of semiconductor egions (EPO) |
| E31.014 | <pre>part of active layer (EPO)Characterized by doping</pre> | s | Multiple quantum well tructure (EPO) |
| E31.015 | <pre>material (EPO)Including, apart from doping</pre> | s | Characterized by amorphous emiconductor layer (EPO) |
| | <pre>material or other impurity, only Group II-VI compound (e.g., CdS, ZnS, HgCdTe) (EPO)</pre> | m | Including, apart from doping aterial or other impurity, only Group IV element or |
| E31.016 | For device having potential or surface barrier (EPO) | C | ompound (e.g., Si-SiGe uperlattice) (EPO) |
| E31.017 | Characterized by doping material (EPO) | | Doping superlattice (e.g., ipi superlattice) (EPO) |
| | <pre>Including ternary compound (e.g., HgCdTe) (EPO)</pre> | s | For device having potential or urface barrier (EPO) |
| E31.019 | Including, apart from doping material or other impurity, only Group III-V compound | E31.039 | Shape of body (EPO) Shape of potential or surface arrier (EPO) |
| E31.02 | (EPO)For device having potential | | naracterized by semiconductor ody crystalline structure or |
| 201.02 | or surface barrier (EPO) | | lane (EPO) |
| E31.021 | <pre>Characterized by doping material GaAlAs, InGaAs, InGaAsP (EPO)</pre> | 0 | Including thin film deposited n metallic or insulating ubstrate (EPO) |
| | <pre>Including ternary or quaternary compound (EPO)</pre> | е | Including only Group IV lement (EPO) |
| E31.023 | Including, apart from doping material or other impurity, | s | Including polycrystalline emiconductor (EPO) |
| =21 004 | only Group IV compound (e.g., SiC) (EPO) | е | Including only Group IV |
| | For device having potential or surface barrier (EPO) | s | Including microcrystalline ilicon (Tc-Si) (EPO) |
| | Characterized by doping material (EPO) | G | Including microcrystalline croup IV compound (e.g., Tc- |
| E31.U26 | Including, apart from doping material or other impurity, | | iGe, Tc-SiC) (EPO) Including amorphous |
| | only compound other than Group | | emiconductor (EPO) |
| | <pre>II-VI, III-V, and IV compound (EPO)</pre> | | Including only Group IV lement (EPO) |
| E31.027 | Comprising only Group I-III- VI chalcopyrite compound | (| Including Group IV compound e.g., SiGe, SiC) (EPO) |
| =21 000 | (e.g., CuInSe2, CuGaSe2, CuInGaSe2) (EPO) | С | Having light-induced haracteristic variation |
| E31.028 | Characterized by doping material (EPO) | | e.g., Staebler-Wronski ffect) (EPO) |
| E31.029 | Comprising only Group IV-VI | | Including other |
| | or II-IV-VI chalcogenide | | onmonocrystalline material |
| ₽31 N3 | compound (e.g., PbSnTe) (EPO) | | e.g., semiconductor particles |
| E31.03 | Characterized by doping material (EPO) | | mbedded in insulating aterial) (EPO) |

| E31.052 | <pre>.Adapted to control current flow through device (e.g., photoresistor) (EPO)</pre> | | With Schottky gate (EPO)Charge-coupled device (CCD) (EPO) |
|---------|-------------------------------------------------------------------------------------------------|---------|-----------------------------------------------------------|
| п21 ОГ2 | | T21 076 | |
| E31.U53 | <pre>For device having potential or surface barrier (e.g., phototransistor) (EPO)</pre> | | Photo MESFET (EPO)With PN homojunction gate |
| ₽21 OE4 | | -21 050 | (EPO) |
| E31.U54 | Device sensitive to infrared, visible, or ultraviolet | | Charge-coupled device (CCD) (EPO) |
| | radiation (EPO) | E31.079 | Field-effect |
| E31.055 | Characterized by only one | | phototransistor (EPO) |
| | potential or surface barrier (EPO) | E31.08 | With PN heterojunction gate (EPO) |
| | <pre>Potential barrier being of point contact type (EPO)</pre> | E31.081 | Charge-coupled device (CCD) (EPO) |
| E31.057 | PN homojunction potential | E31 082 | Field-effect |
| | barrier (EPO) | 131.002 | phototransistor (EPO) |
| E31.058 | Device comprising active | ⊞21 002 | Conductor-insulator- |
| 2017000 | layer formed only by Group II- VI compound (e.g., HgCdTe IR | | semiconductor type (EPO) |
| T21 0E0 | photodiode) (EPO) | | Diode or charge-coupled device (CCD) (EPO) |
| E31.039 | Device comprising active | E31.085 | Metal-insulator- |
| | layer formed only by Group III-V compound (EPO) | | <pre>semiconductor field-effect transistor (EPO)</pre> |
| E31.06 | Device comprising active | E31.086 | Device sensitive to very short |
| | layer formed only by Group IV | | wavelength (e.g., X-ray, |
| | compound (EPO) | | gamma-ray, or corpuscular |
| E31.061 | PIN potential barrier (EPO) | | radiation) (EPO) |
| | Device comprising Group IV | ₽21 NO7 | Bulk-effect radiation |
| | amorphous material (EPO) | E31.007 | |
| F31 063 | Potential barrier working in | | detector (e.g., Ge-Li |
| E31.003 | _ | | compensated PIN gamma-ray |
| | avalanche mode (e.g., | | detector) (EPO) |
| -21 264 | avalanche photodiode) (EPO) | E31.088 | Li-compensated PIN gamma-ray |
| E31.064 | Heterostructure (e.g., | | detector (EPO) |
| | surface absorption or | E31.089 | With surface barrier or |
| | multiplication (SAM) layer) | | shallow PN junction (e.g., |
| | (EPO) | | surface barrier alpha-particle |
| E31.065 | Schottky potential barrier | | detector) (EPO) |
| | (EPO) | E31.09 | With shallow PN junction |
| E31.066 | Metal-semiconductor-metal | | (EPO) |
| | (MSM) Schottky barrier (EPO) | ₽21 NQ1 | Field effect type (e.g., MIS- |
| E31 067 | PN heterojunction potential | E31.091 | |
| | barrier (EPO) | T21 000 | type detector) (EPO) |
| ₽21 060 | Characterized by two | E31.092 | Device being sensitive to very |
| E31.000 | | | short wavelength (e.g., X-ray, |
| | potential or surface barriers | | gamma-ray) (EPO) |
| | (EPO) | E31.093 | Device sensitive to infrared, |
| E31.069 | Bipolar phototransistor | | visible, or ultraviolet |
| | (EPO) | | radiation (EPO) |
| E31.07 | Characterized by at least | E31.094 | Comprising amorphous |
| | three potential barriers (EPO) | | semiconductor (EPO) |
| E31.071 | Photothyristor (EPO) | E31.095 | .Structurally associated with |
| | Static induction type | | electric light source (e.g., |
| | (i.e., SIT device) (EPO) | | electric light source (e.g., electroluminescent light |
| F31 ∩72 | Field effect type (e.g., | | |
| 01.073 | junction field-effect | | source) (EPO) |
| | _ | | |
| | phototransistor) (EPO) | | |

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| E31.096 | Hybrid device containing photosensitive and | E31.111 | Input/output circuit of device (EPO) |
|-----------------|----------------------------------------------------------------------|-----------------|------------------------------------------------------------|
| | electroluminescent components within one single body (EPO) | E31.112 | For device having potential or surface barrier (EPO) |
| E31.097 | Light source controlled by radiation-sensitive | E31.113 | Circuit arrangement of general character for device (EPO) |
| | <pre>semiconductor device (e.g., image converter, image</pre> | E31.114 | For device having potential or surface barrier (EPO) |
| | amplifier, image storage | E31.115 | Position-sensitive and |
| T21 000 | device) (EPO) | | lateral-effect photodetector |
| | Device without potential or surface barrier (EPO) | | <pre>(e.g., quadrant photodiode) (EPO)</pre> |
| E31.099 | Light source being | E31.116 | Device working in avalanche |
| | semiconductor device with | | mode (EPO) |
| | <pre>potential or surface barrier (e.g., light-emitting diode)</pre> | | Encapsulation (EPO) |
| | (EPO) | E31.118 | For device having potential or |
| E31.1 | Device with potential or | 5 21 110 | surface barrier (EPO) |
| | surface barrier (EPO) | | Coatings (EPO) |
| E31.101 | Semiconductor light source and | E31.12 | For device having potential or |
| | radiation-sensitive | ₽21 121 | <pre>surface barrier (EPO)For filtering or shielding</pre> |
| | semiconductor device both | E31.121 | light (e.g., multicolor filter |
| | having potential or surface | | for photodetector) (EPO) |
| | barrier (EPO) | E31.122 | For shielding light (e.g., |
| E31.102 | Formed in or on common | | light-blocking layer, cold |
| 5 21 102 | substrate (EPO) | | shield for infrared detector) |
| E31.103 | Radiation-sensitive semiconductor device | | (EPO) |
| | controlled by light source | E31.123 | For interference filter |
| | (EPO) | | (e.g., multilayer dielectric |
| E31.104 | Radiation-sensitive | -01 104 | filter) (EPO) |
| | semiconductor device without | | Electrode (EPO) |
| | potential or surface barrier | E31.125 | For device having potential or surface barrier (EPO) |
| | (e.g., photoresistor) (EPO) | F31 126 | Transparent conductive layer |
| E31.105 | Light source being | 131.120 | (e.g., transparent conductive |
| | semiconductor device having | | oxide (TCO), indium tin oxide |
| | potential or surface barrier | | (ITO) layer) (EPO) |
| | <pre>(e.g., light-emitting diode) (EPO)</pre> | E31.127 | Optical element associated with |
| E31 106 | Optical potentiometer (EPO) | | device (EPO) |
| | Radiation-sensitive | E31.128 | Device having potential or |
| | semiconductor device with | | surface barrier (EPO) |
| | potential or surface barrier | E31.129 | Comprising luminescent member |
| | (EPO) | | (e.g., fluorescent sheet) (EPO) |
| E31.108 | Semiconductor light source and | E31.13 | Texturized surface (EPO) |
| | radiation-sensitive | | Arrangement for temperature |
| | semiconductor device both | 131.131 | regulation (e.g., cooling, |
| | having potential or surface barrier (EPO) | | heating, or ventilating) (EPO) |
| E31.109 | Formed in or on common | E33.001 | LIGHT-EMITTING SEMICONDUCTOR |
| | substrate (EPO) | | DEVICE HAVING POTENTIAL OR SURFACE BARRIER (EPO) |
| E31.11 | .Detail of nonsemiconductor | E33 002 | .Device characterized by |
| | component of radiation- | 100.002 | semiconductor body (EPO) |
| | sensitive semiconductor device (EPO) | E33.003 | Particular crystalline |
| | (EFO) | | orientation or structure (EPO) |
| | | | |

| E33.004 | Comprising amorphous semiconductor (EPO) | E33.033 | Comprising nitride compound (e.g., AlGaN) (EPO) |
|----------------|------------------------------------------------------------------------|---------|--------------------------------------------------------------|
| E33.005 | <pre>Shape or structure (e.g., shape of epitaxial layer) (EPO)</pre> | E33.034 | With heterojunction (e.g., AlGaN/GaN) (EPO) |
| E33.006 | Shape of semiconductor body (EPO) | E33.035 | Comprising only Group IV compound (e.g., SiC) (EPO) |
| E33.007 | Shape of potential barrier (EPO) | E33.036 | Characterized by doping material (EPO) |
| E33.008 | Multiple quantum well | E33.037 | Comprising compound other than |
| E33.009 | <pre>structure (EPO)Including, apart from doping</pre> | | Group II-VI, III-V, and IV compound (EPO) |
| | materials or other only impurities, Group IV element | E33.038 | Comprising only Group IV-VI compound (EPO) |
| | (e.g., Si-SiGe superlattice) (EPO) | E33.039 | Comprising only Group II-IV- VI compound (EPO) |
| E33.01 | Doped superlattice (e.g., | E33.04 | Comprising only Group I-III- |
| -22 011 | nipi superlattice) (EPO) | | VI compound (EPO) |
| | <pre>For current confinement (EPO)Multiple active regions</pre> | | <pre>Characterized by doping material (EPO)</pre> |
| | <pre>between two electrodes (e.g., stacks) (EPO)</pre> | E33.042 | Comprising only Group IV-VI or II-IV-VI compound (EPO) |
| | Material of active region (EPO) | E33.043 | Physical imperfections (e.g., |
| | In different regions (EPO) | | particular concentration or |
| E33.015 | Comprising only Group IV element (EPO) | | <pre>distribution of impurity) (EPO)</pre> |
| | With heterojunction (EPO) | E33.044 | .Device characterized by their |
| E33.017 | Characterized by doping | | operation (EPO) |
| ₽22 O10 | material (EPO) | E33.045 | Having p-n or hi-lo junction |
| | Including porous Si (EPO)Comprising only Group II-VI | E33 046 | (EPO)P-I-N device (EPO) |
| E33.019 | compound (EPO) | | Having at least two p-n |
| E33.02 | Ternary or quaternary | 133.017 | junctions (EPO) |
| | compound (e.g., CdHgTe) (EPO) | E33.048 | Having heterojunction or graded |
| E33.021 | With heterojunction (EPO) | | gap (EPO) |
| E33.022 | Characterized by doping | E33.049 | Comprising only Group III-V |
| | material (EPO) | | compound (EPO) |
| | Comprising only Group III-V compound (EPO) | | Comprising only Group II-IV compound (EPO) |
| E33.024 | Binary compound (e.g., GaAs) | | Having Schottky barrier (EPO) |
| | (EPO) | | Having MIS barrier layer (EPO) |
| | Including nitride (e.g., GaN) (EPO) | E33.053 | <pre>Characterized by field-effect operation (EPO)</pre> |
| E33.026 | Ternary or quaternary compound (e.g., AlGaAs) (EPO) | E33.054 | Device being superluminescent diode (EPO) |
| E33.027 | With heterojunction (EPO) | E33.055 | .Detail of nonsemiconductor |
| E33.028 | Including nitride (e.g., AlGaN) (EPO) | | component other than light- emitting semiconductor device |
| E33.029 | Characterized by doping | | (EPO) |
| 5 22 22 | material (EPO) | | Packaging (EPO) |
| E33.03 | Nitride compound (EPO) | E33.05/ | Adapted for surface mounting (EPO) |
| E33.U31 | Including ternary or | E33 058 | Housing (EPO) |
| | <pre>quaternary compound (e.g., AlGaAs) (EPO)</pre> | | Encapsulation (EPO) |
| E33.032 | With heterojunction (e.g., | | Coatings (EPO) |
| | AlGaAs/GaAs) (EPO) | | 5 |

| E33.061 | Comprising luminescent | 908 | DRAM CONFIGURATION WITH | |
|-----------------|----------------------------------------------|-------|-------------------------------------------------------------|--|
| | <pre>material (e.g., fluorescent)</pre> | | TRANSISTORS AND CAPACITORS OF | |
| | (EPO) | | PAIRS OF CELLS ALONG A | |
| E33.062 | Electrodes (EPO) | | STRAIGHT LINE BETWEEN ADJACENT | |
| E33.063 | Characterized by material | | BIT LINES | |
| | (EPO) | 909 | MACROCELL ARRAYS (E.G., GATE | |
| E33.064 | Comprising transparent | | ARRAYS WITH VARIABLE SIZE OR | |
| | conductive layers (e.g., | | CONFIGURATION OF CELLS) | |
| | transparent conductive oxides | 910 | DIODE ARRAYS (E.G., DIODE READ- | |
| | (TCO), indium tin oxide (ITO)) | | ONLY MEMORY ARRAY) | |
| | (EPO) | 911 | LIGHT SENSITIVE ARRAY ADAPTED TO | |
| E33.065 | Characterized by shape (EPO) | | BE SCANNED BY ELECTRON BEAM | |
| E33.066 | Electrical contact or lead | | (E.G., VIDICON DEVICE) | |
| | (e.g., lead frame) (EPO) | 912 | CHARGE TRANSFER DEVICE USING BOTH | |
| E33.067 | Means for light extraction or | | ELECTRON AND HOLE SIGNAL | |
| | guiding (EPO) | | CARRIERS | |
| E33.068 | Integrated with device (e.g., | 913 | WITH MEANS TO ABSORB OR LOCALIZE | |
| | back surface reflector, lens) | | UNWANTED IMPURITIES OR DEFECTS | |
| | (EPO) | | FROM SEMICONDUCTORS (E.G., | |
| E33.069 | Comprising resonant cavity | | HEAVY METAL GETTERING) | |
| | structure (e.g., Bragg | 914 | POLYSILICON CONTAINING OXYGEN, | |
| | reflector pair) (EPO) | | NITROGEN, OR CARBON (E.G., | |
| E33.07 | Comprising window layer (EPO) | 015 | SIPOS) | |
| E33.071 | Not integrated with device | 915 | WITH TITANIUM NITRIDE PORTION OR | |
| | (EPO) | 016 | REGION | |
| | Reflective means (EPO) | 916 | NARROW BAND GAP SEMICONDUCTOR | |
| E33.073 | Refractive means (e.g., lens) | 917 | MATERIAL (<< 1EV) PLURAL DOPANTS OF SAME | |
| | (EPO) | 917 | CONDUCTIVITY TYPE IN SAME | |
| E33.074 | Scattering means (e.g., | | REGION | |
| 5 22 055 | surface roughening) (EPO) | 918 | LIGHT EMITTING REGENERATIVE | |
| E33.075 | With means for cooling or | 710 | SWITCHING DEVICE (E.G., LIGHT | |
| E22 076 | heating (EPO) With means for light detecting | | EMITTING SCR) ARRAYS, | |
| E33.070 | (e.g., photodetector) (EPO) | | CIRCUITRY, ETC. | |
| E33 077 | Monolithic integration with | 919 | ELEMENTS OF SIMILAR CONSTRUCTION | |
| E33.077 | photosensitive device (EPO) | | CONNECTED IN SERIES OR | |
| | photosensitive device (EFO) | | PARALLEL TO AVERAGE OUT | |
| CDOSS_D | EFERENCE ART COLLECTIONS | | MANUFACTURING VARIATIONS IN | |
| CHARACTERISTICS | | | | |
| 900 | MOSFET TYPE GATE SIDEWALL | 920 | CONDUCTOR LAYERS ON DIFFERENT | |
| 500 | INSULATING SPACER | | LEVELS CONNECTED IN PARALLEL | |
| 901 | MOSFET SUBSTRATE BIAS | | (E.G., TO REDUCE RESISTANCE) | |
| 902 | FET WITH METAL SOURCE REGION | 921 | RADIATION HARDENED SEMICONDUCTOR | |
| 903 | FET CONFIGURATION ADAPTED FOR USE | | DEVICE | |
| 303 | AS STATIC MEMORY CELL | 922 | WITH MEANS TO PREVENT INSPECTION | |
| 904 | .With passive components (e.g., | | OF OR TAMPERING WITH AN | |
| | polysilicon resistors) | | INTEGRATED CIRCUIT (E.G., | |
| 905 | PLURAL DRAM CELLS SHARE COMMON | 923 | "SMART CARD" ANTI-TAMPER) WITH MEANS TO OPTIMIZE ELECTRICAL | |
| | CONTACT OR COMMON TRENCH | J 4 3 | CONDUCTOR CURRENT CARRYING | |
| 906 | DRAM WITH CAPACITOR ELECTRODES | | CAPACITY (E.G., PARTICULAR | |
| | USED FOR ACCESSING (E.G., BIT | | CONDUCTOR ASPECT RATIO) | |
| | LINE IS CAPACITOR PLATE) | 924 | WITH PASSIVE DEVICE (E.G., | |
| 907 | FOLDED BIT LINE DRAM | | CAPACITOR), OR BATTERY AS | |
| | CONFIGURATION | | INTEGRAL PART OF HOUSING OR | |
| | | | HOUSING ELEMENT (E.G., CAP) | |
| | | | | |

| 925 | BRIDGE RECTIFIER MODULE |
|-----|----------------------------------|
| 926 | ELONGATED LEAD EXTENDING AXIALLY |
| | THROUGH ANOTHER ELONGATED LEAD |
| 927 | DIFFERENT DOPING LEVELS IN |
| | DIFFERENT PARTS OF PN JUNCTION |
| | TO PRODUCE SHAPED DEPLETION |
| | LAYER |
| 928 | WITH SHORTED PN OR SCHOTTKY |
| | JUNCTION OTHER THAN EMITTER |
| | JUNCTION |
| 929 | PN JUNCTION ISOLATED INTEGRATED |
| | CIRCUIT WITH ISOLATION WALLS |
| | HAVING MINIMUM DOPANT |
| | CONCENTRATION AT INTERMEDIATE |
| | DEPTH IN EPITAXIAL LAYER |
| | (E.G., DIFFUSED FROM BOTH |
| | SURFACES OF EPITAXIAL LAYER) |
| 930 | THERMOELECTRIC (E.G., PELTIER |
| | EFFECT) COOLING |

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